| DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD | RRRRRRRRRRR RRRRRRRRRRR RRRRRRRRRRRRRR | | VVV VVV VVV VVV | | RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR |
|--|--|-----------|--------------------|----------------|--|
| DDD DDD | RRR RRR | iii | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRR RRR | 111 | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRR RRR | 111 | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRR RRR | iii | VVV VVV | ĒĒĒ | RRR RRR |
| DDD DDD | RRR RRR | III | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRRRRRRRRRR | III | VVV VVV | EEEEEEEEEE | RRRRRRRRRRR |
| DDD DDD | RRRRRRRRRRR | 111 | VVV VVV | EEEEEEEEEEE | RRRRRRRRRRR |
| DDD DDD | RRRRRRRRRRRR RRR RRR | 111 | VVV VVV | EEEEEEEEEEE | RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR |
| DDD DDD | RRR RRR | 111 | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRR RRR | iii | VVV VVV | ĒĒĒ | RRR RRR |
| DDD DDD | RRR RRR | III | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRR RRR | III | VVV VVV | EEE | RRR RRR |
| DDD DDD | RRR RRR | !!! | VVV | EEE | RRR RRR |
| DDDDDDDDDDDDDDD | RRR RRR | 111111111 | VVV | EEEEEEEEEEEEEE | RRR RRR |
| DDDDDDDDDDDD | RRR RRR | 111111111 | VVV | EEEEEEEEEEEE | RRR RRR |

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| MM MM MMMM MMMM MMMMM MMMMM MM MM MM MM MM | BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB | XX |
|--|--|--|
| | | \$ |

FILEID**MBXDRIVER

| MM | BBBBBBBB BBBBBBBB BB BB BB BB BB BB BBBBBB | XX | DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD | RRRRRRRR RR | | VV | | RR RR RRRRRRRR RRRRRRRR RR RR RR RR RR RR RR | RR RR RR |
|--|---|--|--|--|--------|--|-----------|---|----------------|
| MM MM | 8888888 | XX XX | DDDDDDDD | RR RR | 111111 | νν | EEEEEEEEE | | RF |

| MBXDRIVER Table of contents | - SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 |
|--|---|
| (2) 318 (3) 448 (4) 534 (5) 610 (6) 714 (7) 754 (8) 930 (9) 985 (10) 1016 (11) 1092 (12) 1171 (13) 1271 (14) 1383 (15) 1426 | CANCELIO - CANCEL I/O ON MAILBOX UNIT CHECKIO - CHECK READ AND WRITE ACCESS AND PARAMETERS FDTREAD - READ FUNCTION DECISION ROUTINE FDTSET - HANDLE SET MODE FUNCTIONS FDTEOF - WRITE EOF MESSAGE TO MAILBOX FDTWRITE - WRITE OPERATION FDT ROUTINE ALLOC FAIL/MAILBOX FULL - WRITE FDT ROUTINE FAILURES DALLOC BLOCKS - DEĀLLOCATE SHARED MEMORY BLOCKS STARTIO - STARTIO OPERATION FINISHREAD - FINISH READ I/O OPERATION MBX\$INT - INTERRUPT DISPATCHER NOTIFY - NOTIFY OTHER PROCESSORS OF CONDITIONS ALLOC IRPE - ALLOCATE AN I/O REQUEST PACKET EXTENSION DALLOC IRPE - DEALLOCATE AN I/O REQUEST PACKET EXTENSION |

10

222222222222355555

5015534567

(1)

.TITLE MBXDRIVER - SHARED MEMORY MAILBOX DEVICE DRIVER .IDENT 'V04-001'

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: FACILITY:

VAX/VMS EXECUTIVE

ABSTRACT:

THIS MODULE CONTAINS THE SHARED MEMORY MAILBOX DRIVER I/O ROUTINES.

AUTHOR: LEN KAWELL 13-MAR-1979

MODIFIED BY:

V04-001 ACG0467 Andrew C. Goldstein, 12-Sep-1984 22:07 fix protection holes in QIO device protection check

V03-017 LMP0271 L. Mark Pilant, 12-Jul-1984 12:28 Note, in the ORB, that shared memory mailboxes cannot have ACLs.

V03-016 LMP0266 L. Mark Pilant, 27-Jun-1984 11:38 Add \$CCBDEF for V03-015.

V03-015 LMP0265 L. Mark Pilant, 26-Jun-1984 15:27 Only do a protection check for the first I/O to the channel.

V03-014 RAS0300 Ron Schaefer 19-Jun-1984
Add DEV\$M_NNM characteristic to DECHAR2 so that these
devices will have the "node\$" prefix.

| | 0000 | 58 : | v03-013 | WMC0001 Wayne Cardoza Previous update destroyed R4 before mute | 17-May-1984 x calls. |
|---|--|---|--|---|---|
| 0000 60 0000 61 0000 62 0000 63 0000 64 0000 65 0000 66 0000 67 0000 68 | 61 62 63 64 65 66 67 68 | v03-012 | WMC0001 Previous update destroyed R4 before muter MK0001 Todd M. Katz When deleting the logical name associated delete the logical name block by calling instead of LNMSDELETE. Doing so will enstakes place as if the system service SDE to delete the logical name. In other work target logical name be deleted, but so work mode aliases. LMP0221 L. Mark Pilant, Change UCB\$L_OWNUIC to ORB\$L_OWNER and LORB\$W_PROT. ROW0277 Ralph O. Weber Implement use of IO\$M_NORSWAIT modifier waits. DMW4039 DMWalp Intergate new logical name structures. ROW0172 Ralph O. Weber Change device type to DT\$_SHRMBX. ROW0170 Ralph O. Weber Insert delete mailbox functionality from CANCELIO. This moves the mailbox specified delete a mailbox from \$DASSGN into this cultivated by the contract of th | 21-Apr-1984 d with a mailbox, LNM\$DELETE_LNMB ure that this deletion LLNM had been called ds, not only will the fill all outer access | |
| | 0000 0000 0000 0000 0000 | 70 ; 71 ; 72 ; | v03-011 | LMP0221 L. Mark Pilant, Change UCB\$L_OWNUIC to ORB\$L_OWNER and UORB\$W_PROT. | 27-Mar-1984 9:12 ICB\$W_VPROT to |
| | 0000 0000 0000 0000 | 74 : 75 : 76 : | v03-010 | ROW0277 Ralph O. Weber Implement use of IO\$M_NORSWAIT modifier waits. | 11-JAN-1984 to prevent resource |
| | 0000 | 78 79 | v03-009 | DMW4039 DMWalp Intergate new logical name structures. | 31-May-1983 |
| | 0000 0000 0000 | 81 82 | v03-008 | ROW0172 Ralph O. Weber Change device type to DT\$_SHRMBX. | 10-APR-1983 |
| | 0000 0000 0000 0000 | 84 : 85 : 86 : | v03-007 | ROW0170 Ralph O. Weber Insert delete mailbox functionality from CANCELIO. This moves the mailbox specifi delete a mailbox from \$DASSGN into this | 12-MAR-1983 I IOC\$DELMBX in ic knowledge of how to driver. |
| | 0000 0000 0000 | 89 : | v03-006 | CWH1002 CW Hobbs Use extended pid in iosb process ids. | 1-Mar-1983 |
| | 0000 0000 0000 0000 | 92 93 94 95 | v03-005 | ROW49973 Ralph O. Weber Make all changes necessary to have contr EXE\$IORSNWAIT at IPL\$_ASTDEL rather than necessary to conform with internal change | 29-001-1982 rol transfered to IPL\$_SYNCH. This is ges in EXE\$IORSNWAIT. |
| | 0000 0000 0000 0000 0000 | 97 98 99 100 101 | v03-004 | ROW0118 Ralph O. Weber Change FINISHREAD to return SS\$_BUFFEROV SS\$_DATAOVERUN. SS\$_BUFFEROVF is an alt Its use in place of SS\$_DATAOVERUN will overflow condition to be reported to intwithout hassling uninterested programs to be reported to interested programs. | 7-JUL-1982 IF instead of sernate success status. allow the buffer serested programs with an error status. |
| | 0000 0000 0000 | 104 | v03-003 | KDM0002 Kathleen D. Morse Added \$DEVDEF and \$PRVDEF. | 28-Jun-1982 |
| | 0000 0000 0000 0000 0000 | 106 : 107 : 108 : 109 : 110 : | v03-002 | ROW0105 Ralph O. Weber Change FINISHREAD to return SS\$_DATAOVER bytes in mail box message being read excin user supplied buffer. | 18-JUN-1982 RUN when number of seeds number of bytes |
| | 0000 0000 0000 0000 | 112 113 114 | v03-001 | ROW0104 Ralph O. Weber Make several changes to improve handling messages in mailboxes. Change READCHECK | 18-JUN-1982 of zero length 10 and WRITECHECKIO |
| | | | | | |

```
to allow zero-byte messages, and provide a dummy buffer address for such messages. Add function code information to shared memory message so that zero length messages can be differentiated from end-of-file messages. This change is distributed as part of MBXDRIVER.EXE ECO 1 in Version 3.1.
                116
                1201234567890123456789
112234567890123456789
                                                               KDM0074 Kathleen D. Morse 8-Jan-1982
Clear IDB pointer to UCB for shared memory mailbox,
                                            V02-008 KDM0074
                                                               when no more references to the UCB and it is going
                                                               to be deallocated.
                                            V02-007 KDM0067
                                                                                                     Kathleen D. Morse
                                                                                                                                                             10-Nov-1981
                                                               Fix stack and synchronization problems.
0000
                                            V02-006 STJ0026 Steven T. Jeffreys 05-Feb-19
Modified FDTSET to default to IOSM_WRTATTN if no
                                                                                                                                                              05-Feb-1981
0000
                                                               function modifier is present.
0000
                                            V02-005 STJ0020
                                                                                                                                                              20-Jan-1981
                                                                                                     Steven T. Jeffreys
                                                               Modified FDTSET routine to handle SETPROT function.
0000
                140
                              EXTERNAL SYMBOLS
                1443445678901234456789012345678901
                                                                                                                            DEFINE AST CONTROL BLOCK
DEFINE CONDITIONAL ASSEMBLY
                                            SACBDEF
                                            SCADEF
                                                                                                                           CANCEL REASON CODES
DEFINE CHANNEL CONTROL BLOCK OFFSETS
DEFINE CHANNEL REQUEST BLOCK
DEFINE COMPLEX CHAINED BUFFERS
DEFINE DEVICE CLASSES & TYPES
DEFINE DDB
DEFINE DEVICE TYPES
DEFINE DYNAMIC BLOCK TYPES
DEFINE DYNAMIC BLOCK
                                            SCANDEF
                                            SCCBDEF
                                            SCRBDEF
                                            $CXBDEF
                                            $DCDEF
                                            $DDBDEF
                                            $DEVDEF
                                                                                                                          DEFINE DYNAMIC BLOCK TYPES
DEFINE FORK BLOCK
DEFINE INTERRUPT DISPATCHER
DEFINE IVO PACKET OFFSETS
DEFINE I/O PACKET EXTENSION OFFSETS
DEFINE IPL NUMBERS
DEFINE MAILBOX
OBJECT'S RIGHTS BLOCK OFFSETS
DEFINE PCB OFFSETS
DEFINE PROCESSOR REGISTERS
DEFINE PRIORITY INCREMENTS
DEFINE PRIORITY INCREMENTS
DEFINE PRIVILEGE NUMBERS
DEFINE PRIVILEGE NUMBERS
DEFINE RESOURCE NUMBERS
DEFINE SHARED MEMORY CONTROL BLOCK
DEFINE SHARED MEMORY DATAPAGE
DEFINE SYSTEM STATUS CODES
DEFINE UCB OFFSETS
DEFINE INTERRUPT TRANSFER VECTOR
                                            SDYNDEF
                                            $FKBDEF
                                            $IDBDEF
                                            $IODEF
$IRPDEF
                                            SIRPEDEF
                                             SIPLDEF
                                             SMBXDEF
                                             SORBDEF
                                             $PCBDEF
                                             SPRDEF
                                             SPRIDEF
                                             SPRQDEF
                                             $PRVDEF
                                             $RSNDEF
                                             SSHBDEF
                                             $SHDDEF
                                             SSSDEF
SUCBDEF
                                             SVECDEF
```

MB

```
16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
```

```
0000
0000
0000
0000
0000
0000
0000
                        172
173
174
175
                                 LOCAL DEFINITIONS
                                MACRO TO SET PORT FLAG CORRESPONDING TO THIS PROCESSOR
                                           .MACRO
                                                     SET_PORTFLAG MASK, ?LABEL
                                          BBSSI
                                                     UCB$L_MB_PORT(R5), MASK, LABEL
                        181
182
183
184
185
                             LABEL:
                                          . ENDM
                                                     SET_PORTFLAG
                              ; MACRO TO CLEAR PORT FLAG CORRESPONDING TO THIS PROCESSOR
              0000
                                           MACRO
                                                     CLR_PORTFLAG MASK, ?LABEL
              0000
                                          BBCCI
                                                     UCB$L_MB_PORT(R5), MASK, LABEL
              0000
                        188
                             LABEL:
                        189
                                          .ENDM
                                                     CLR_PORTFLAG
              0000
                        190
              0000
                        191
              0000
              0000
                              : DEVICE SPECIFIC I/O REQUEST PACKET EXTENSION DEFINITIONS
                        194
              0000
              0000
                                          SDEFINI IRPE
                        196
              0000
                                = FKB$K_LENGTH
DEF IRPE$W_MB_PORTS .BLKW
00000018
              0000
                                                                                           (BEGINNING IS FORK BLOCK)
              0018
001A
001C
0020
0024
0024
                        198 SDEF
                                                                                           PORTS TO NOTIFY (1 BIT/PORT)
                                          IRPESU_MB_RQTYP .BLKW
IRPESL_MB_PARAM .BLKL
IRPESL_MB_PORT .BLKL
                        199
                              $DEF
                                                                                           REQUEST TYPE CODE
                        $DEF
                                                                                           REQUEST PARAMETER
                              $DEF
                                                                                           NEXT PORT TO NOTIFY
                                          SDEFEND IRPE
              0000
0000
0000
                                MAILBOX MESSAGE BUFFER DEFINITION
              0000
                                          SINCE THE SHARED MEMORY POOL IS ONLY ALLOCATABLE IN FIXED
              0000
                                          SIZE BLOCKS, A MESSAGE IS STORED AS A LIST OF CHAINED BLOCKS.
              0000
                                          SDEFINI MSG
                                         SDEFINI MSG
MSG_Q_MSGLINK
MSG_L_POSTIOBUF
MSG_L_POSTUBUF
MSG_W_SIZE
MSG_B_TYPE
MSG_B_TYPE
MSG_W_LENGTH
MSG_W_MSGLENGTH
MSG_L_CHAINLINK
MSG_L_IRPSEQ
MSG_L_PID
MSG_B_FUNC
MSG_B_MESSAGE
SDEFEND_MSG
                                                                                           MESSAGE QUEUE LINK
1/0 POST 1/0 BUFFER ADDRESS
                              SDEF
                                                                 .BLKL
                                                                                             I/O POST USER BUFFER ADDRESS
                              SDEF
                                                                 .BLKL
                                                                                          SIZE OF BLOCK
TYPE OF BLOCK (DYNSC SHRBUFIO)
PORT NUMBER OF MESSAGE WRITER
LENGTH OF MESSAGE IN BLOCK
TOTAL LENGTH OF MESSAGE DATA
LINK TO NEXT CHAINED BLOCK
                              SDEF
SDEF
SDEF
SDEF
SDEF
SDEF
SDEF
SDEF
                                                                  .BLKW
                                                                  .BLKB
                                                                  .BLKB
                                                                  .BLKW
                                                                 .BLKW
              0010
0014
0018
0010
0010
0010
                        .BLKL
                                                                                           IRP SEQUENCE NUMBER OF MESSAGE WRITER
                                                                 .BLKL
                                                                                           PID OF MESSAGE WRITER
                                                                  .BLKL
                                                                 .BLKB
                                                                                           ORIGINATING FUNCTION CODE
                              SDEF
                                                                                           START OF MESSAGE IN BLOCK
                                          SINCE THE MESSAGE IS PASSED DIRECTLY TO 1/O POST, IT MUST
                                          CONFORM TO THE DEFINITION FOR A COMPLEX CHAINED BUFFER
```

ME V

```
DRIVER DISPATCH TABLE
                                                      ; DRIVER DISPATCH TABLE
              DDTAB
                        DEVNAM=MBX,-
START=STARTIO,-
                                                         DEVICE NAME
                                                          START I/O OPERATION
                                                         FUNCTION DECISION TABLE
                        FUNCTB=FUNCTABLE,-
                        CANCEL=CANCELIO
                                                          CANCEL I/O OPERATION
    FUNCTION DECISION TABLE
    FUNCTABLE:
                                                        FUNCTION DECISION TABLE
              FUNCTAB .-
                                                        LEGAL FUNCTIONS
                                                          SET ATTENTION AST
                        WRITEOF .-
READLBLK , WRITELBLK ,-
                                                         WRITE END-OF-FILE
                                                         READ/WRITE LOGICAL BLOCKS
READ/WRITE VIRTUAL BLOCKS
READ/WRITE PHYSICAL BLOCKS
                         READVBLK, WRITEVBLK, -
                         READPBLK, WRITEPBLK>
              FUNCTAB
                        READUBLK, WRITELBLK, --
READUBLK, WRITEVBLK, --
                                                         BUFFERED 1/0 FUNCTIONS
                                                         READ/WRITE LOGICAL BLOCKS
READ/WRITE VIRTUAL BLOCKS
READ/WRITE PHYSICAL BLOCKS
                         READPBLK, WRITEPBLK>
311
312
313
314
315
316
                                                        READ FOT ACTION ROUTINE
              FUNCTAB FOTREAD,-
                          <READLBLK, READPBLK, READVBLK>
                                                        WRITE FOT ACTION ROUTINE
              FUNCTAB FOTWRITE,-
              FUNCTAB FOTSET, <SETMODE> ; SET A
                                                      : SET ATTENTION AST FOT ROUTINE
                                                      ; WRITE END-OF-FILE FOT ROUTINE
              FUNCTAB FDTEOF, <WRITEOF>
```

01 64 A5

16 64 A5 0C A3

28 A3

0CF0 8F 58 02

60

56

6A 52

E1 D1 12 B1 12

CHECK CURRENT READ I/O REQUEST AND COMPLETE IF CANCELLED

R6, IRPSW_CHAN(R3)

BBC

BNEQ

CMPW BNEQ #UCB\$V_BSY,UCB\$W_STS(R5),20\$; READ IN PROGRESS?
PCB\$L_PID(R4),IRP\$L_PID(R3); IS IT FROM CANCELLING PROCESS?
20\$; IF NEQ THEN NO

CHANNEL MATCH?

IF NEQ THEN NO

| | - SHARE | D MEMORY MAILBOX DE O - CANCEL I/O ON P | G 10 EVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 Page 8 MAILBOX UNIT 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2 (2) |
|--|--|--|---|
| | | | |
| 00000000 · GF | 7D 009 | 9C 375 MC | OVQ #SS\$_ABORT,RO : SET STATUS TO ABORT SB G^IOC\$REQCOM : COMPLETE THE REQUEST |
| | 00/ | AS 378 CHECK WE | RITE I/O REQUESTS AND COMPLETE IF CANCELLED |
| 52 00A0 C5 | 9E 00/ | A5 380 208: MC | OVAB UCBSL_MB_WIOQFL(R5),R2 : GET ADDRESS OF WRITE 1/O QUEUE OVL R2,R0 : COPY LIST HEAD ADDRESS |
| 52 00A0 C5 50 52 52 62 52 50 | 00 00/ | AD 382 308: MG | OVL (R2),R2 ; GET ADDRESS OF LIST ENTRY |
| 10 | 9E 00/ 00 00/ 00 00/ 01 006 13 006 01 006 | B3 384 B6 | MPL RO, R2 : END OF LIST? EQL 40\$: IF YES THEN DONE |
| 60 A4 0C A2 F1 | 008 | A5 379 : MC A5 380 20\$: MC AA 381 | MPL PCB\$L_PID(R4) : REQUEST FROM CANCELLING PROCESS? IRP\$L_PID(R2) NEQ 30\$: IF NO THEN SEARCH MORE |
| 28 A2 56 | B1 008 | BC 388 CF | NEQ 30\$ IF NO THEN SEARCH MORE MPW R6, IRP\$W_CHAN(R2) ; CHANNEL MATCH? NEQ 30\$ IF NEQ THEN NO |
| 28 A2 56 53 62 38 A3 20 00000000 GF | 12 006 81 006 12 000 70 000 16 000 | C2 390 RE | EMQUE (R2).R3 : REMOVE PACKET FROM QUEUE |
| 00000000° GF | 16 000 | ČÝ 392 J. CF 393 RE | OVQ #SS\$ ABORT, IRP\$L_IOST1(R3); SET STATUS TO ABORT SB G^COM\$POST ; COMPLETE THE OPERATION RB 20\$; SEARCH LIST FROM THE START |
| | 11 000 000 000 9E 000 16 000 | D1 394 : CHECK AT | TTENTION AST REQUESTS AND DELETE IF CANCELLED |
| 57 0090 C5 | 9E 000 | D1 396 ; D1 397 408: MC | |
| 00000000 GF 57 0094 C5 | 9E 000 16 000 9E 000 16 000 | D6 398 JS | SB G^COMSFLOSHATTNS ; FLUSH ATTENTION AST'S OVAB UCBSL_MB_RAST(R5),R7 ; GET ADDRESS OF READ AST'S |
| 0000000°GF | 9E 000 | E1 400 JS | |
| | 000 | E7 402 ; CHECK 11 E7 403 ; ANY REM/ | F MAILBOX CONTROL BLOCK SHOULD BE DEALLOCATED. IF SO, DEALLOCATE AINING MESSAGE BLOCKS AND MARK THE MAILBOX AS NO LONGER VALID. |
| 58 01 58 | 006 006 01 12 006 85 006 | D1 395; CHECK AT D1 396; D1 397 40\$: MC D6 398 DC 399 E1 400 E7 401; E7 402; CHECK II E7 403; ANY REM/ E7 405 EA 406 EC 407 EF 408 | MPL #CANSC_DASSGN, R8 : Deassigning channel? NEQ 698 : Branch if not channel deassign. |
| 5C A5 | D1 006 12 006 B5 006 12 006 | EC 407 TS | THE HITHER DEST (DS) . TO PATARABLE COURT TARAST |
| 51 68 A5 01 | E1 000 | | NEQ 69\$ BC #UCB\$V DELMBX, - Branch if mailbox is not zero. BC #UCB\$V DELMBX, - Branch if mailbox is not |
| 50 0090 05 | DO 005 D7 005 D0 005 | F6 411 MC | OVL UCB\$L_MB_SHB(R5).RO : GET ADDRESS OF SHB ECL SHB\$L_REFCNT(RO) : DECREMENT SHARED MEMORY REFERENCE COUNT |
| 51 OC AO | DO 006 DO 006 DO 006 DO 016 E7 016 B5 016 B6 011 5E 016 30 016 | FB 412 DE FE 413 MC 02 414 LC | OVL SHB\$L DATAPAGE(RO).R1 : GET DATAPAGE ADDRESS |
| 00 0C A2 0098 C5 0C A2 0C A2 18 08 A2 02 50 09 A2 | DO 01 | 20 415 MG | OCK #SHD\$V MBXLCK,SHD\$B_FLAGS(R1); LOCK MAILBOX TABLE OVL UCB\$L_MB_MBX(R5),R2 ; GET MAILBOX CONTROL BLOCK ADDRESS RCCI UCB\$L_MR_PORT(R5)_MRX\$W_REF(R2)_50\$: CLEAR_PORT'S REFERENCE |
| OC A2 | DO 012 E7 013 B5 013 12 013 8A 013 9A 013 B6 013 1D 014 30 014 | 25 416 2C 417 50\$: TS 2F 418 | BCCI UCB\$L_MB_PORT(R5),MBX\$W_REF(R2),50\$; CLEAR PORT'S REFERENCE STW MBX\$W_REF(R2); ANY OTHER REFERENCES? NEQ 70\$: IF NEO YES |
| 08 A2 02 50 09 A2 | 8A 01 | 31 419 B1 | NEQ 70\$ IF NEQ YES ICB #MBX\$M_VALID.MBX\$B_FLAGS(R2); CLEAR VALID FLAG OVZBL MBX\$B_CREATPORT(R2),R0; GET_CREATOR_PORT_NUMBER |
| 50 A140 58 62 07 0330 | 86 01 5E 01 | 39 421 IN 30 422 608: RE | OVZBL MBX\$B TREATPORT(R2),R0 ; GET CREATOR PORT NUMBER NCW SHD\$W_MBXQUOTA(R1)[R0] ; RESTORE CREATOR'S QUOTA EMQHI MBX\$Q_MSG(R2),R11 ; GET ADDRESS OF NEXT MESSAGE BLOCK VS 70\$; IF VS NO MORE BLOCKS SBW DALLOC_BLOCKS ; DEALLOCATE THE MESSAGE BLOCK(S) |
| 07 | 1D 014 | 40 423 BY | VS 708 : IF VS NO MORE BLOCKS SBW DALLOC_BLOCKS : DEALLOCATE THE MESSAGE BLOCK(S) |
| f 6 3F | 11 014 | 45 425 PE | RB 60\$ RB 900\$; Exit branch assist. |
| | 014 | 49 427 49 428 70\$: UN | |
| 57 24 A5 56 54 A5 57 20 A7 | 11 014 014 014 014 015 30 015 00 015 | 2F 418 BN 31 419 B1 35 420 MC 39 421 IN 30 422 60\$: RE 40 423 BN 42 424 BS 45 425 BF 47 426 69\$: BF 49 427 70\$: UN 52 429 MC 54 431 MC | OVL UCB\$L_CRB(R5),R7 ; CLEAR OUT THE POINTER IN THE OVZWL UCB\$W_UNIT(R5),R6 ; IDB TO THIS UCB, PREVENTING A |
| 57 2C A7 | 00 019 | DA 451 MC | OVL <crbse_intd+vecsl_idb>(R7),R7; RACE CONDITION BY ANOTHER</crbse_intd+vecsl_idb> |

00000000 GF

00000000 GF

00000000°GF

00000000 GF

01B2 01B7

01B9

16 E9

BBS

JSB BLBC

RO, ERROR

0A 08 A6

59

```
- SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro VO4-00 CMECKIG - CHECK READ AND WRITE ACCESS AN 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
                                                                                                                               Page
                448
449 :++
450 : RE
       018D
018D
018D
                                  .SBITL CHECKIO - CHECK READ AND WRITE ACCESS AND PARAMETERS
                        READCHECKIO - CHECK READ ACCESS AND PARAMETERS WRITECHECKIO - CHECK WRITE ACCESS AND PARAMETERS
                         FUNCTIONAL DESCRIPTION:
                      THIS ROUTINE IS USED BY THE READ AND WRITE FOT ROUTINES TO VALIDATE THE I/O REQUEST. THE CHECKS ARE:
                                 o ACCESS TO UNIT BY UIC
                                 O MESSAGE SIZE WITHIN MAX MESSAGE SIZE
                                 O BUFFER ACCESSABLE
                465
4667
468
469
470
471
473
474
475
                      ; ZERO LENGTH TRANSFERS AND ACCESS VIOLATIONS CAUSE COMPLETIONS HERE.
                      : INPUTS:
                                 RO-R2 = SCRATCH
R3 = PACKET ADDRESS
                                  R4 = PCB ADDRESS
                                 R5 = UCB ADDRESS
                                 R6 = CCB ADDRESS
R7 = FUNCTION CODE
                                 R8 = ADDRESS OF FOT TABLE ENTRY FOR THIS ROUTINE
                                 R9-R11 = SCRATCH
                                 AP = ADDRESS OF THE FIRST QIO PARAMETER
                      OUTPUTS:
                                 R3 = PACKET ADDRESS
                 482
483
484
485
                                 R4 = PCB ADDRESS
                                 R5 = UCB ADDRESS
                                 IRP$L_MEDIA(R3) = BUFFER ADDRESS.
IRP$W_BCNT(R3) = BUFFER SIZE.
      018D
018D
018D
0193
019A
                     READCHECKIO:
                                                                                 CHECK FOR READ ACCESS
                                 PUSHAB G^EXESREADCHK
MOVAB G^EXESCHKRDACCES,R9
MOVL #CCBSV_RDCHKDON,R10
 9F
9E
00
11
                491
493
495
496
497
498
498
498
498
5501
5503
                                                                                 SET UP FOR BUFFER READ I/O ACCESS
                                                                                 SET UP FOR UNIT READ ACCESS
       019D
                                             CHECKIO
                                 BRB
       019F
019F
                      WRITECHECKIO:
                                                                                 CHECK FOR WRITE ACCESS
 9E
00
9F
                                            G^EXESCHKWRTACCES,R9
#CCBSV_WRTCHKDON,R10
                                                                                 SET UP FOR BUFFER WRITE 1/0 ACCESS
                                 MOVAB
       01A6
                                 MOVL
       01A9
                                 PUSHAB
                                            G*EXESORITECHK
                                                                                 SET UP FOR UNIT WRITE ACCESS
                                                                                 CHECK 1/O ACCESS AND PARAMETERS
       01AF
                      CHECKIO:
 B4
E0
                                 CLRW
                                             IRPSW_BOFF(R3)
R10,CCB$B_STS(R6),10$
                                                                                 RESET QUOTA
```

SKIP CHECK IF ALREADY DONE

R4 - PCB ADDRESS
R5 - UCB ADDRESS
CHECK READ/WRITE ACCESS
BR IF ACCESS FAILURE

| | SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 HECKIO - CHECK READ AND WRITE ACCESS AN 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER | Page |
|---|---|-----------------------------------|
| 00 08 A6 5A 51 04 AC 0E 42 A5 51 12 38 A3 50 | E2 01BC 505 3C 01C1 506 10\$: MOVZWL P2(AP),R1 BEQL ZEROLENGTH : IF EQL THEN COMPLETE HERE CMPW R1,UCB\$W_DEVBUFSIZ(R5) CMPW R1,UCB\$W_DEVBUFSIZ(R5) MESSAGE SIZE IN RANGE? IF GTRU THEN NO MOVL P1(AP),R0 : GET BUFFER ADDRESS MOVL P1(AP),R0 : GET BUFFER ADDRESS SAVE BUFFER ADDRESS RETURN AND CHECK BUFFER ACCORDANCE RSB : RETURN AND CHECK BUFFER ACCORDANCE RETURN AND CHECK BUFFER AC | CESS |
| | 01D5 514 :+ 01D5 515 : PROCESS ZERO LENGTH TRANSFERS 01D5 516 : 01D5 517 : for a zero byte transfer, a dummy buffer (whose address is the constructed) of the current stack) of zero bytes length is constructed. The results of the current stack of this buffer because the previous of the current stack. 01D5 520 : may not have access to the current stack. 01D5 521 :- 01D5 522 | rrent top lormal lus caller |
| 38 A3 8E 38 A3 6E | 01D5 520; may not have access to the current stack. 01D5 521; 01D5 522 01D5 523 ZEROLENGTH: D5 01D5 524 TSTL (SP)+ D6 01D7 525 CLRL IRP\$L_BCNT(R3); Set zero byte count. D6 01DA 526 MOVAB (SP), IRP\$L_MEDIA(R3); Set top-of-stack buffer access to the current stack. D7 01DF 528 RSB ROVZWL MSS\$_MBTOOSML,R0 RSB RETURN directly to routing access to the current stack. D8 01DF 528 RSD RSD RSD RSD RSD RSD RSD RSD RSD RETURN directly to routing access to the current stack. D9 01DF 528 RSD | ldress. |
| 50 019C 8F 00000000 GF | 01DF 529 TOOSMALL: 3C 01DF 530 MOVZWL #SS\$_MBTOOSML,RO : SET BOX TOO SMALL FOR MESS 01E4 531 ERROR: : ERROR - ABORT THE I/O REQU 17 01E4 532 JMP G^EXE\$ABORTIO : ABORT THE I/O | |

(3)

```
- SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro VO4-00 FDTREAD - READ FUNCTION DECISION ROUTINE 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
        01EA
01EA
01EA
                                       .SBTTL FOTREAD - READ FUNCTION DECISION ROUTINE
```

: FDTREAD - FUNCTION DECISION ROUTINE FOR READ OPERATIONS FUNCTIONAL DESCRIPTION:

THE REQUEST IS FIRST CHECKED FOR READ ACCESS TO THE MAILBOX AND WRITE ACCESS TO THE SPECIFIED BUFFER. THE PACKET IS THEN QUEUED TO THE UNIT'S I/O QUEUE (UCB\$L IOQFL) FOR PROCESSING WHEN THE UNIT IS NOT BUSY, IN OTHER WORDS, WHEN ANY PREVIOUS READ REQUESTS ON THIS PROCESSOR HAVE BEEN SATISFIED.

IF THE FUNCTION MODIFIER IOSM NOW IS SPECIFIED, THE MAILBOX IS CHECKED TO SEE IF ANY MESSAGES ARE WAITING. IF THERE AREN'T MESSAGES, THE REQUEST IS COMPLETED WITH FAILURE, OTHERWISE IT IS QUEUED AS FOR A NORMAL READ REQUEST.

INPUTS:

DIEA

01EA 01EA 01EA

01EA 01EA

01EA 01EA

01EA 01EA

01EA 01EA 01EA 01EA

01EA 01EA

01EA 01EA

01EA

01EA 01EA 01EA

01EA

RO-R2 = SCRATCH R3 = 1/0 PACKET ADDRESS R4 = CURRENT PCB ADDRESS R5 = UCB ADDRESS R6 = CCB ADDRESS R7 = FUNCTION CODE R8 = ADDRESS OF FDT TABLE ENTRY FOR THIS ROUTINE R9-R11 = SCRATCH AP = FIRST QIO PARAMETER ADDRESS

: OUTPUTS:

THE PACKET IS QUEUED VIA "EXESQIODRVPKT" OR THE REQUEST IS COMPLETED WITH AN ERROR VIA "EXESABORTIO" OR "EXESFINISHIOC"

: STATUS CODES:

SS\$_NOPRIV - USER DOES NOT HAVE PRIVILEGE TO READ MAILBOX SS\$_ACCVIO - BUFFER ACCESS VIOLATION SS\$_MBTOOSML - REQUEST EXCEEDS THE MAXIMUM MESSAGE SIZE SSS_ENDOFFILE - NO MESSAGE AVAILABLE AND IOSM_NOW SPECIFIED SSS_NORMAL - NORMAL STATUS

FOTREAD:

VALIDATE THE REQUEST BSBB READCHECKIO ; VALIDATE THE REQUES WIRPSM_MBXIO, IRPSW_STS(R3); SET MAILBOX READ BISW

; UPDATE MEASUREMENT COUNTER IF ENABLED

IF NE CAS MEASURE INCL G*PMS\$GL_MBREADS INCL : COUNT MAILBOX READS .ENDC

ALLOCATE TWO I/O PACKET EXTENSIONS TO USE AS FORK BLOCKS IF WE'RE FORCED TO WAIT WHEN: 1) NOTIFYING OTHER PROCESSOR OF WAITING READER 2) NOTIFYING OTHER PROCESSOR WHEN MESSAGE IS READ

2A A3 0400 8F

> 00000002 00000000 GF

5945 5996 5996 5999 6001 6007 6007 6007 BBC #10\$V_NOW, IRP\$W_FUNC(R3), 10\$; BR IF NOT 'NOW''
SETIPL #IPL\$_MAILBOX ; RAISE IPL TO MAKE SURE NO 17 20 A3 06 E1 OTHER REQUEST SNEAKS IN QUEUE 0098 C5 62 08 0870 8F UCB\$L_MB_MBX(R5),R2
MBX\$Q_MSG(R2)
10\$
#SS\$_ENDOFFILE,R0
G^EXE\$FINISHIOC DO D5 12 30 17 MOVL 52 GET MAILBOX ADDRESS ANY MESSAGES IN MAILBOX? IF NEQ THEN YES BNEQ MOVZWL ; SET NO TRANSFER AND STATUS 0000000 GF JMP : COMPLETE THE I/O 021D 105: 00000000 GF 17 021D JMP G*EXE\$QIODRVPKT : QUEUE PACKET TO STARTIO

```
- SHARED MEMORY MAILBOX DEVICE DRIVER FOTSET - HANDLE SET MODE FUNCTIONS
                                                                                                                         VAX/VMS Macro V04-00
[DRIVER.SRC]MBXDRIVER.MAR;2
                                                             .SBTTL FDTSET - HANDLE SET MODE FUNCTIONS
                                                   FDTSET - HANDLE SET MODE FUNCTIONS
                                                   FUNCTIONAL DESCRIPTION:
                                                   THIS ROUTINE IMPLEMENTS THE IOS SETMODE FUNCTIONS.
THE DIFFERENT FUNCTIONS ARE SELECTED BY A FUNCTION CODE MODIFIER.
THE FUNCTIONS ARE:
                                                             IOSM_SETPROT
IOSM_READATTN
                                                                                     - SET VOLUME PROTECTION
- SET READ ATTENTION AST
                                                             IOSM_WRTATTN
                                                                                     - SET WRITE ATTENTION AST
                                                   INPUTS:
                                                            RO-R2 = SCRATCH
R3 = I/O PACKET ADDRESS
                                                             R4 = CURRENT PCB
                                                             R5 = UCB ADDRESS FOR MAILBOX UNIT
                                                             AP = ADDRESS OF QIO PARAMETER BLOCK
                                                   OUTPUTS:
                                                            NONE .
                                                   STATUS RETURNS:
                                                            SS$_NORMAL - SUCCESSUFL COMPLETION
SS$_INSFMEM - INSUFICIENT MEMORY TO ALLOCATE AST BLOCK
SS$_EXQUOTA - AST QUOTA EXCEEDED
SS$_ILLIOFUNC - ILLEGAL SET MODE FUNCTION
SS$_NOPRIV - THE USER CANNOT SET THE VOLUME PROTECTION
                                                FDTSET:
                                                                                                              : SET RECEIVE AST FUNCTION
                                                   SEE IF THIS IS A SETPROT FUNCTION.
                                                                         #IOSV_SETPROT,-
IRPSW_FUNC(R3),50$
                        E0
                                                                                                                 BRANCH IF SETPROT FUNCTION
       65 20 A3
                                                   SEE IF USER CAN READ THIS MAILBOX
                                                                                                                 R4 - PCB ADDRESS
R5 - UCB ADDRESS
   00000000°GF
B3 50
                                                             JSB
                                                                         G^EXESCHKRDACCES
                                                                                                                 CHECK READ ACCESS TO UNIT
                        16
E9
                                                            BLBC
                                                                         RO, ERROR
                                                                                                                 IF LOW CLEAR THEN ERROR
                                                   CREATE AN AST CONTROL BLOCK AND ENTER IT IN APPROPRIATE ATTENTION LIST
                                                                        UCB$L_MB_WAST(R5),R7; ASSUME WRITE AST LIST ADDR
#10$V_READATTN, IRP$W_FUNC(R3),10$; BR IF NOT READ AST
UCB$L_MB_RAST(R5),R7; GET_ADDR OF READ AST LIST
                                          660
661
662
663
664
05720
                        DE
E1
DE
DD
                                                             MOVAL
        A3
0094
                                                             BBC
                                                             MOVAL
                                                                                                                 SAVE PCB ADDRESS
SAVE AST LIST HEAD ADDRESS
ENTER AN AST REQUEST IN LIST
                                                105:
                                                             PUSHL
                                                             PUSHL
                                                                         G^COM$SETATTNAST
   00000000
                                                             JSB
                                                             POPL
                                                                                                                 GET AST LIST HEAD ADDRESS
```

| | - SHARED MEMOR | Y MAILBOX DEVICE E SET MODE FUNCT | N 10 DRIVER 16-SEP-1984 00 IONS 12-SEP-1984 23 | 0:02:15 VAX/VMS Macro V04-00 Page 3:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2 | 15 (5) |
|---|--|--|--|--|--------|
| | 024D 66 024D 66 024D 67 024D 67 024D 67 024D 67 | SET WAITING SET WA | READY EXISTS. IF IT DOES | ILBOX AND CHECK IF ATTENTION S. CLEAR THE WAITING ATTENTION AST. FLAG MUST BE SET BEFORE KED FOR WAITING ATTENTIONS AFTER WAITING FLAG. | |
| 14 20 A3 07 | DO 024D 67 | MOVL BBS | UCB\$L_MB_MBX(R5),R2 #10\$V_READATTN,IRP\$W_FU | GET ADDR OF MAILBOX JNC(R3),20\$; BR IF READ AST | |
| 62 22 13 | 0257 67 0257 67 0258 67 13 0260 67 0262 68 11 0269 68 | 8 TSTE | RTFLAG MBX\$W_WRITAST(R2) MBX\$Q_MSG(R2) 40\$ RTFLAG MBX\$W_WRITAST(R2) 30\$ | : IF EQL THEN NO - JUST COMPLETE : CLEAR NOTIFY FLAG : DELIVER THE AST | |
| OE A2 | 026B 68 026B 68 85 0272 68 13 0275 68 | S BEQL | RTFLAG MBX\$W_READAST(R2) MBX\$W_READER(R2) 40\$ RTFLAG MBX\$W_READAST(R2) | READ ATTENTION AST REQUEST SET FLAG TO NOTIFY IF READ OCCURS ANY READERS WAITING? IF EQL THEN NO - JUST COMPLETE | |
| 0000000°GF | 16 027E 68 0284 68 0284 69 | 8 JSB | G*COMSDELATTNAST | ; DELIVER THE AST IMMEDIATELY | |
| 00000000°GF | 8EDO 0284 69 | COMPLETE THE | R4 G*EXESFINISHIOC SETPROT FUNCTION | : RESTORE PCB ADDRESS : COMPLETE THE I/O | |
| 51 1C A5 00BC C4 | 3C 028D 69 00 0290 69 01 0294 69 | 7 50\$: MOVZWL MOVL CMPL | #SS\$ NOPRIV.RO UCB\$E_ORB(R5).R1 PCB\$L_UIC(R4) ORB\$L_OWNER(R1); | : ASSUME NO PRIVILEGE : GET ORB ADDRESS : IS THIS THE VOLUME OWNER? | |
| 50 04 AC 52 0098 C5 0B A1 01 18 A1 50 1A A2 50 50 01 CE 1D DD 6C B4 FF23 | BO 029B 70 DO 029F 70 02A4 70 | 12 51\$: MOVW MOVL SETIPL BISB2 MOVW MOVW MOVZWL BRB BBS | P2(AP),R0 UCB\$L_MB_MBX(R5),R2 UCB\$B_DIPL(R5) #ORB\$M_PROT_16,ORB\$B_FL R0,ORB\$W_PROT(R1) R0,MBX\$W_PROT(R2) | BRANCH IF NOT GET THE PROTECTION MASK GET MBX ADDRESS BLOCK DEVICE INTERRUPTS AGS(R1); PROTECTION WORD NOT VECTOR SET THE NEW PROTECTION MASK SET SECOND COPY OF PROTECTION MASK SET SUCCESS STATUS COMPLETE THE I/O BRANCH IF USER HAS BYPASS ABORT THE I/O | |

```
- SHARED MEMORY MAILBOX DEVICE DRIVER FOTEOF - WRITE EOF MESSAGE TO MAILBOX
                                                                                                                 VAX/VMS Macro V04-00
[DRIVER.SRC]MBXDRIVER.MAR;2
                                                                                                                                                                     16
                                     714
715
716
717
                                                        .SBTTL FDTEOF - WRITE EOF MESSAGE TO MAILBOX
                                           FDTEOF - WRITE EOF MESSAGE TO THE MAILBOX
                                              FUNCTIONAL DESCRIPTION:
                                              THIS IS THE FOT ROUTINE FOR IOSWRITEOF. THE ACTION IS TO BUILD A ZERO LENGTH MESSAGE AND TO INSERT IT IN THE MAILBOX. THIS MESSAGE, WHEN READ RESULTS IN AN SSS_ENDOFILE STATUS RETURN.
                                     INPUTS:
                                                        RO-R2 = SCRATCH
                                                       R3 = I/O PACKET ADDRESS
R4 = CURRENT PCB ADDRESS
R5 = MAILBOX UCB ADDRESS
                           0201
                                                       R6 = CCB ADDRESS
R7 = FUNCTION CODE
R8 = ADDRESS OF FDT TABLE ENTRY FOR THIS ROUTINE
                                                       R9-R11 = SCRATCH
                           02C1
02C1
02C1
02C1
02C1
02C1
                                                       AP = ADDRESS OF USER ARGUMENT BLOCK AT "P1"
                                              OUTPUTS:
                                                        IRP$L MEDIA(R3) = FAKE BUFFER ADDRESS.
                                                       IRP$W_BCNT(R3) = ZERO BUFFER SIZE.
                                                       THE I/O IS COMPLETED IN THE WRITE FOT LOGIC. ( SEE BELOW )
                                           FDTEOF:
         30 A3
                    04
                                                       CLRL
                                                                                                          SET NO TRANSFER AND NO QUOTA
                                                                   IRP$W_BOFF(R3)
                           02C4
02C4
02CA
02CA
02CD
02D0
                                                                                                          R4 - PCB ADDRESS
R5 - UCB ADDRESS
00000000 GF
                    16
E9
D4
9E
11
                                                                   GAEXESCHKWRTACCES
                                                        JSB
                                                                                                          CHECK WRITE ACCESS TO UNIT
         09 50
32 A3
                                                       BLBC
                                                                                                          IF ERROR THEN BRANCH
                                                                   IRPSW_BCNT(R3)
(SP), IRPSL_MEDIA(R3)
WRITE
                                                       CLRL
                                                                                                          SET ZERO LENGTH BUFFER
SET FAKE ADDR OF BUFFER
             6E
06
  38 A3
                                                       MOVAB
                                                       BRB
                                                                                                          WRITE THE MESSAGE
                                     752 10$:
          FFOB
                           0206
                                                       BRW
                                                                   ERROR
                                                                                                       : CONTINUE
```

MB:

FEC3

32 38 A3 5B

54 A3 03F1

52 009C C5

```
- SHARED MEMORY MAILBOX DEVICE DRIVER FOTWRITE - WRITE OPERATION FOT ROUTINE
                                                                                                                          VAX/VMS Macro V04-00
[DRIVER.SRC]MBXDRIVER.MAR;2
                                                                                                                                                                                                 (7)
          .SBTTL FDTWRITE - WRITE OPERATION FDT ROUTINE
                        FDTWRITE -- FUNCTION DECISION ACTION ROUTINE FOR WRITE FUNCTIONS
                                   FUNCTIONAL DESCRIPTION:
                                   THE USER REQUEST IS VALIDATED FOR PRIVILEGE, SIZE, ACCESS AND AVAILABLE SPACE. IF VALID, A BUFFERED I/O BLOCK IS ALLOCATED (IMPLIED RESOURCE WAIT). THE BLOCK IS SET UP AND QUEUED TO THE UNIT MESSAGE LIST. IF THE UNIT IS BUSY, THE OUTSTANDING READ OPERATION IS COMPLETED DIRECTLY. IN THE CASE OF "WRITENOW" FUNCTIONS THE I/O IS COMPLETED BEFORE THE MESSAGE IS QUEUED. OTHERWISE THE READ COMPLETE ROUTINE COMPLETES THE MESSAGE ASSOCIATED WRITE.
                                   INPUTS:
                                               R3 = I/O PACKET ADDRESS
R4 = CURRENT PCB ADDRESS
R5 = UCB ADDRESS
                                               R6 = CCB ADDRESS
R7 = FUNCTION CODE
                                                R8 = ADDRESS OF FDT TABLE ENTRY FOR THIS ROUTINE
          R9-R11 = SCRATCH
                                                AP = ADDRESS OF USER ARGUMENT BLOCK AT "P1"
                                   OUTPUTS:
                        780
7783
77883
77885
77889
77889
77889
77899
77899
77899
80123
8038
                                               THE I/O IS COMPLETED IN ERROR, THE I/O IS RESTARTED BECAUSE OF RESOURCE WAIT, OR THE I/O IS COMPETED NORMALLY.
                                   STATUS RETURNS:
                                               SS$_MBTOOSML - MESSAGE IS TOO BIG
SS$_ACCVIO - BUFFER ACCESS VIOLATION ( "EXESWRITECHK" )
SS$_MBFULL - MAILBOX IS FULL
SS$_NOPRIV - USER DOES NOT HAVE WRITE PRIVILEGE
SS$_NORMAL - SUCCESSFUL STATUS
SS$_INSFMEM - NO MEMORY FOR BUFFER ALLOCATION
                               FDTWRITE:
                                                BSBW
                                                                                                               : CHECK OPERATION PARAMETERS
  30
                                                               WRITECHECKIO
                                WRITE:
  3C
D0
D4
                                                                                                                  R9 = SIZE OF USER DATA
R10 = ADDRESS OF USER DATA
R11 = ADDRESS OF FIRST BLOCK
                                                               IRPSW_BCNT(R3),R9
IRPSL_MEDIA(R3),R10
                                                MOVZWL
                                                MOVL
                                                CLRL
                               ALLOCATE AN I/O PACKET EXTENSION TO USE AS A FORK BLOCK IF WE'RE FORCED TO WAIT WHEN NOTIFYING OTHER PROCESSORS OF THE AVAILABILITY OF A MESSAGE.
                                                               IRP$L_EXTEND(R3)
ALLOC_IRPE
  30
                                                                                                                   SET NO EXTENSION YET
                                                                                                               : ALLOCATE A EXTENSION
                                                BSBW
                        805
806
807
808
809
810
                                   ALLOCATE SHARED MEMORY POOL BLOCK
                                ALLOC_BLOCK:
                                                                                                                  ALLOCATE SHARED MEMORY BLOCK
GET ADDR OF SHB
                                                               UCB$L_MB_SHB(R5),R2
SHB$L_DATAPAGE(R2),R1
                                                MOVL
                                                MOVL
   DO
                                                                                                               : GET ADDR OF DATAPAGE
```

MB

| | - SHARED MEMOR | ORY MAILBOX DEVICE (| D 11 DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 Page 18 |
|--|---|---|--|
| | FDTWRITE - WR | | |
| 50 03 | DO 02F5 8 | B11 MOVL B12 DSBINT | #RSNS_NPDYNMEM,RO ; GET RESOURCE NUMBER #IPLS_SYNCH ; PREVENT SCHEDULING SHDSW_RESWAIT(R1)[R0],-(SP); SAVE ADDR OF WAIT MASK TFLAG 3(SP) ; ASSUME ALLOCATION FAILURE |
| 7E 00A8 C140 | 3E 02FE 8 | MOVAW SET POR | SHD\$W RESWAIT (R1) [R0], -(SP); SAVE ADDR OF WAIT MASK |
| 00000000 GF | 16 030B 8 | 515 | : (AVDIDS MISSING NOTIFICATION) |
| 03 50 011F | 16 030B 8 E8 0311 8 31 0314 8 | 816 JSB 817 BLBS 818 BRW | RO.10\$; IF LBS SUCCESS ALLOC FAIL ; ELSE - FAILURE |
| | 0317 8 | 19 10\$: CLR POR | TELAG A(SP)+ : CLEAR FLAG SINCE BLOCK ORTAINED |
| 5B | D5 0320 8 | PNEO | TE NEO NO |
| 5B 52 0E A2 59 | D5 0320 8 12 0322 8 D0 0324 8 B0 0327 8 11 032B 8 | MOVL | R2.R11 R9.MSG_W_MSGLENGTH(R2) ; SAVE ADDRESS OF FIRST BLOCK SETUP_BLOCK ; SET SIZE OF MESSAGE DATA |
| 05 | 11 032B 8 | 825 826 20\$: 827 SUBL3 | SETUP_BLOCK ; |
| 10 A8 52 5B | C3 032D 8 | 327 SUBL3 | R11,R2,MSG_L_CHAINLINK(R8) ; SET OFFSET FROM FIRST BLOCK TO NEW BLOC |
| | 0332 8 | SET UP MESSAGE | E BLOCK |
| 0A A2 80 8F | 90 0332 8 | ST SETUP_BLOCK: | #DYNSC_SHRBUFIO,MSG_B_TYPE(R2); SET TYPE OF BLOCK |
| OB A2 OOA8 C5 | 90 0337 8 | 833 | - No. 18 |
| | 033D 8 | ASSUME | UCB\$L_MB_PORT(R5), MSG_B_PORT(R2); SET WRITER'S PORT NUMBER PRQ\$C_MINLENGTH GE MSG_B_MESSAGE+4; NEED ROOM FOR SOME DATA #MSG_B_MESSAGE,R1 ; COMPUTE SIZE FOR DATA IN BLOCK R9,MSG_W_LENGTH(R2) ; ASSUME ALL DATA FITS IN BLOCK R9,R1 ; IS DATA TOO BIG? 10\$; IF LEQ NO - DATA FITS R1,MSG_W_LENGTH(R2) ; ELSE - SET LOWER SIZE |
| 0C A2 59 51 59 | BO 0340 8 | 336 SUBL 337 MOVW 338 CMPL | R9.MSG_W_LENGTH(R2) : ASSUME ALL DATA FITS IN BLOCK |
| 0C A2 59 51 59 04 0C A2 51 | C2 033D 8 B0 0340 8 D1 0344 8 15 0347 8 B0 0349 8 | BLEQ BLEQ MOVW | 10\$; IF LEQ NO - DATA FITS R1,MSG_W_LENGTH(R2) ; ELSE - SET LOWER SIZE |
| | 034D 84 | 341 10\$: | MSG_L_CHAINLINK(R2) ; CLEAR CHAIN LINK POINTER |
| 10 A2 50 A3 14 A2 | 04 0340 84 00 0350 84 0353 84 | 342 CLRL 343 MOVL | IRP\$L_SEQNUM(R3) - ; SET WRITER'S IRP SEQUENCE NUMBER MSG_L_IRPSEQ(R2) ; |
| 03 20 A3 06 14 A2 | | | #IOSV NOW.IRPSW FUNC(R3).158 : BR IF NOT 'NOW' |
| | 035D 84 | 347 348 15\$: MOVL | MSG_L_IRPSEQ(R2) ; CLEAR WRITER'S IRP SEQUENCE NUMBER ; INDICATES NO NEED TO NOTIFY WRITER PCB\$L_EPID(R4) ; INSERT EXTENDED PID OF WRITER |
| 10 A2 20 A3 | 90 0362 85 | 349 350 MOVB | MSG L PID (R2) |
| 58 52 | 0367 8 00 0367 8 | 51 MOVL | MSG B_FUNC(R2); R2,R8; SAVE ADDRESS OF BLOCK |
| ~ ~ | 036A 8 | 553 : 354 : COPY DATA FROM | M USER BUFFER TO SHARED MEMORY |
| 30 | 036A 8 | PUSHR | #AMCRO DE DA DES . SAVE DEGISTEDS |
| 1D A2 6A | BB 036A 85 28 036C 85 036F 85 | 57 MOVC3 | MSG W_LENGTH(R2) (R10),MSG_B_MESSAGE(R2); W^M <r2,r3,r4,r5> MSG_W_LENGTH(R2),R0 RO,R10 RO,R10 MOVE FROM USER TO SHARED MEMORY RESTORE REGISTERS GET SIZE OF MESSAGE AGAIN INCREMENT USER BUFFER ADDR</r2,r3,r4,r5> |
| 30 | BA 0372 85 | 359 POPR 360 MOVZWL | #^M <r2,r3,r4,r5> : RESTORE REGISTERS MSG_W_LENGTH(R2),R0 : GET SIZE OF MESSAGE AGAIN</r2,r3,r4,r5> |
| 5A 50 59 50 | CO 0378 86 | 361 ADDL 362 SUBL | RO,R10 : INCREMENT USER BUFFER ADDR RO,R9 : DECREMENT USER BUFFER SIZE |
| 50 OC A2 5A 50 59 50 03 FF69 | 035D 86 0360 86 90 0362 86 0367 86 036A 86 0372 86 0378 86 037 | 863 BEQL 864 BRW | CHÉCK QUOTAS : IF EQL. NO MORE ALLOC BLOCK : ELSE, ÁLLOCATE ANOTHER BLOCK |
| | 0383 86 0383 86 | 845 846 847 848 848 859 850 851 852 860 855 856 857 858 859 860 861 862 863 864 865 865 866 867 866 867 | |
| | 0383 8 | 167 ; | |

| | - SHAR | RED MEMORY MA | AILBOX DEVICE | DRIVER ROUTINE | 16-SEP-1984 12-SEP-1984 | 4 00:02 | :15 VAX/ | VMS Macro V04-00 VER.SRCJMBXDRIVER. | MAR;2 Page | 1 |
|---|--------|--|---|--|--|--------------------|---|---|------------------------|---|
| 51 009C C5 51 04 A1 50 02 7E 00A8 C140 | DO 0 | 0383 868 CI 0383 869 0389 870 038E 871 0392 872 0395 873 0398 874 | HECK_QUOTAS: DSBIN' MOVL MOVL MOVL MOVAW SET_PO | #IPLS_M UCB\$L_M SHB\$L_D #RSNS_M SHD\$W_R ORTFLAG 8(| AILBOX B_SHB(R5),R1 ATAPAGE(R1),I AILBOX,R0 ESWAIT(R1)[R(SP) | R1 0],-(\$F | GET ADDRES GET RESOUI) ; SAVE ASSUME MA | LBOX QUOTAS AILBOX I/O INTERRU SS OF SHB SS OF DATAPAGE RCE NUMBER ADDRESS OF WAIT MA ILBOX FULL FAILURE ISSING NOTIFICATIO | SK | |
| 52 0098 C5 | DO 0 | 3A2 876 3A7 877 | HOVL | UCB\$L M | B MBX(R5),R2 | SB_FLAG | GET ADDRES | SS OF MAILBOX NTERLOCK QUOTA CHE | | |
| | |)3(4 878 ;)3(4 879 ;)3(4 880 ;)3(4 881 ; | SEE IF MESS | | | | | IN OTHER WORDS, IS | | |
| 18 A2 32 A3 78 | B1 0 | 03C4 881; 03C4 882 03C9 883 03CB 884; | CMPW BGTRU | IRPSW B | CNT(R3),MBX\$(_FULL | W_BUFFO | IF GTR TH | MESSAGE FIT? EN NO | | |
| | 0 | 3CB 885 : | | | AND BUFFER Q | | | | | |
| 16 A2 32 A3 18 A2 | R6 0 | 3CB 887 3CE 888 | INCW | MBX\$W_M | SGCNT (R2) CNT (R3),- | | INCREMENT DECREASE | MESSAGE COUNT BUFFER QUOTA | | |
| 44 A5 16 A2 | во 0 | 3CB 884; 3CB 885; 3CB 886; 3CB 887 3CE 888 3D1 889 3D3 890 3DB 891 3E0 892; | UNLOCI MOVW CLR_P(| MBX\$V MBX\$V MBX\$W M | QUOTALCK, MBX: SGCNT(R2), UCI SP)+ | SB FLAG BSC_DEV | SS(R2); UI DEPEND(R5 CLEAR WAI | MESSAGE COUNT BUFFER QUOTA NLOCK MAILBOX QUOT); SAVE MESSAGE C T FLAG AS MAILBOX | AS OUNT NOT FULL | |
| | 9 | 3E6 894 : | QUEUE THE MI | SSAGE. M | | D BEFOR | E WE LOOK | FOR ANYONE WAITIN | | |
| 007A | 30 0 | 03E6 895 ; 03E6 896 ; 03E6 898 03F5 899 03F8 900 | QRETRI INSQT ENBIN BSBW | (R11),M | BX\$Q_MSG(R2) | | RE-ENABLE | SSAGE IN QUEUE INTERRUPTS E MESSAGE BLOCKS | | |
| 50 0394 8F 00000000 GF | 3C 0 | 3FB 901 3400 902 | MOVZWI JMP | . #SSS_BA | DQUEUEHDR,RO INISHIOC | | SET FAILU | RE STATUS | | |
| | | 0400 902 0406 903 : 0406 904 : 0406 905 : | NOTIFY OTHER | INTEREST | ED PROCESSOR | S THAT | A MESSAGE | WAS WRITTEN. | | |
| 0237 28 | BB 0 | 0406 905 20 0406 906 20 0406 907 0408 908 0408 909 | PUSHR BSBW POPR | #^M <r3, NOTIFY #^M<r3,< td=""><td>R5> WRITE R5></td><td></td><td>SAVE REGIS NOTIFY IN RESTORE RE</td><td>STERS TERESTED PROCESSOR EGISTERS</td><td>S</td><td></td></r3,<></r3, | R5> WRITE R5> | | SAVE REGIS NOTIFY IN RESTORE RE | STERS TERESTED PROCESSOR EGISTERS | S | |
| | ò | 040D 910 : 040D 911 : 040D 912 : | UPDATE MEASE | REMENT CO | UNTER IF ENAL | BLED | | | | |
| 0000000°GF | 0002 d | 0400 902 0406 903 0406 905 0406 907 0408 908 0408 909 0408 910 0400 911 0400 913 0400 913 0413 916 0413 918 0413 919 0413 920 0418 921 | IF NI INCL ENDC | GAS MEAS GAPMSSG | URE L_MBWRITES | : | CHECK FOR | MEASUREMENT ENABL LBOX WRITES | ED | |
| | Š | 0413 917 0413 918 | IF I/O REQUI | ST SPECIF | IED IOSM NOW | BE COM | ETE IT. | ELSE, INSERT 1/0 EN MESSAGE IS READ | | |
| 0E 20 A3 06 00A0 C5 63 | EO G | 0413 919 : 0413 920 0418 921 | BBS | #10\$V N (R3),00 | OW, IRPSW_FUNG BSL_MB_WIOQF | C(R3),3 L(R5); | OS; BR IF | WRITE NOW RP IN WRITE I/O QUINTERRUPTS CALLER | EUE | |
| 0000000°GF | 17 (| 0420 923 | ENBIN | G*EXESQ | IORETURN | : | RE-ENABLE RETURN TO | CALLER | | |
| | | | | | | | | | | |

MBXDRIVER V04-001 - SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 Page 20 FDTWRITE - WRITE OPERATION FDT ROUTINE 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2 (7)

50 30 A3 D0 0429 926 MOVL IRPSW BCNT-2(R3),R0 50 01 B0 042D 927 MOVW #SS\$ NORMAL,R0 00000000 GF 17 0430 928 JMP G^EXESFINISHIOC

: RE-ENABLE INTERRUPTS : SET BYTE COUNT IN 2ND WORD : SET STATUS IN 1ST WORD : COMPLETE THE I/O MB Sy

```
- SHARED MEMORY MAILBOX DEVICE DRIVER
                        - SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 ALLOC_FAIL/MAILBOX_FULL - WRITE FOT ROUT 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
                                                                                                                                                                                            21 (8)
                                                                 .SBTTL ALLOC_FAIL/MAILBOX_FULL - WRITE FDT ROUTINE FAILURES
                                                       ALLOC_FAIL - SHARED MEMORY POOL ALLOCATION FAILURE.
                                                       MAILBOX_FULL - MAILBOX QUOTA FAILURE.
                                                       INPUTS:
                                                                 R3 = IRP ADDRESS.
R5 = UCB ADDRESS.
                                             R11 = FIRST SHARED MEMORY MESSAGE BLOCK ADDRESS.
                                                      ALLOC_FAIL:

(SP) = ADDRESS OF SHARED MEMORY WAIT MASK.

4(SP) = OLD IPL (IPL$_ASTDEL)
                                                      MAILBOX_FULL:
(SP) = OLD IPL (IPL$ MAILBOX)
4(SP) = ADDRESS OF SHARED MEMORY WAIT MASK
                                                                 8(SP) = OLD IPL (IPL$_ASTDEL)
                                                       OUTPUTS:
                                                                 ALL SHARED MEMORY MESSAGE BLOCKS IN THE CHAIN ARE DEALLOCATED AND THE REQUESTING PROCESS IS PUT IN A WAIT STATE UNTIL THE
                                                                 NEEDED RESOURCE BECOMES AVAILABLE.
                                                   ALLOC_FAIL:
                                                                                                                         SHARED MEMORY ALLOCATION FAILURE
POP ADDR. OF WAIT MASK OFF STACK
SET FAILURE STATUS, FORGETTING IPL
SET RESOURCE TO AWAIT
                                                                              #4,SP
#S$$ INSFMEM,(SP)
#RSN$ NPDYNMEM,R1
                          CO
3C
9A
11
                                                                 ADDL
                 8F
03
                                                                 MOVZWL
                                                                 MOVZBL
                                                                              SHMRES_WAIT
                                                                 BRB
                                                   MAILBOX_FULL:

UNLOCK #MBX$V_QUOTALCK,MBX$B_FLAGS(R2); UNLOCK QUOTAS

ADDL #4.SP : POP MASK ADDRESS OFF STACK

MOVZWL #S$$_MBFULL,(SP) : SET FAILURES STATUS, FORGETTING IPL
                                                                             #4,SP
#SS$_MBFULL,(SP)
#IPL$_SYNCH
#RSN$_MAILBOX,R1
                          CO
         5E
08D8
                                                                 SETIPL
                                                                                                                         SET RESOURCE TO AWAIT WAIT FOR SHARED MEMORY RESOURCE
                           9A
          51
                  02
                                                                 MOVZBL
                                                   SHMRES_WAIT:
                           10
                  14
                                                                              DALLOC_BLOCKS
                                                                                                                         DEALLOCATE SHARED MEMORY BLOCKS
                                                      WAIT FOR NEEDED RESOURCE, BY DEALLOCATING I/O PACKETS, RESTORING I/O QUOTAS AND COUNTS AND INSERTING PROCESS IN MWAIT STATE QUEUE. WHEN RESOURCE BECOMES AVAILABLE, PROCESS WILL BE RESTARTED AT
                                                       BEGININNING OF $QIO REQUEST.
                                                   RES_WAIT:
                                                                                                                         WAIT FOR NEEDED RESOURCE
DEALLOCATE IRPE'S
                                                                 BSBW
                                                                              DALLOC_IRPE
                                                                                                                         GET FAILURE STATUS
SYNCHRONIZE FOR GIO BACKOUT & WAIT
IS NO RESOURCE WAIT MODIFIER SET?
                       8EDO
                                                                 POPL
                                                                              #IPLS_ASTDEL
#IOSV_NORSWAIT, -
IRPSW_FUNC(R3), 698
G^EXESIORSNWAIT
                                                                 SETIPL
06 20 A3
                           E0
                                                                 BBS
                                                                                                                         BRANCH IF MODIFER IS SET.
ELSE, DO POSSIBLE RESOURCE WAIT.
ABORT I/O TO AVOID RESOURCE WAITS.
   00000000 GF
                          17
                                                                  JMP
                                                                              G^EXESABORTIO
                                                                  JMP
```

Sy

IR IR

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- SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 DALLOC_BLOCKS - DEALLOCATE SHARED MEMORY 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
                                                     .SBTTL DALLOC_BLOCKS - DEALLOCATE SHARED MEMORY BLOCKS
                                  DALLOC_BLOCKS - DEALLOCATE ANY SHARED MEMORY BLOCKS
                                            INPUTS:
                                                    R11 = FIRST BLOCK ADDRESS.
                                            OUTPUTS:
                                                    ALL SHARED MEMORY MESSAGE BLOCKS IN THE CHAIN ARE DEALLOCATED.
                                                    R1,R2,R3 ARE PRESERVED.
                                         DALLOC_BLOCKS:
                                                                                                     DEALLOCATE SHARED MEMORY
SAVE REGISTERS
GET ADDRESS OF FIRST BLOCK
                                                    PUSHR
                                                                #^M<R1,R2,R3>
                                                     MOVL
                                                                R11,RC
      50
                                                     BEQL
                                                                20$
                                                                                                   : IF EQL NOT ALLOCATED
                                         105:
                                                                                                    GET OFFSET TO NEXT BLOCK
DEALLOCATE THE BLOCK
COMPUTE ADDRESS OF NEXT BLOCK
IS THERE A NEXT BLOCK?
IF NEQ YES
            GF
5B
5A
EE
                                                                MSG L CHAINLINK(RO),R10;
GERESDEASHARED
R11,R10,R0;
5A 10
                   D0
16
C1
D5
12
                                                     MOVL
                                                     JSB
                                                     ADDL3
     5A
                                                    TSTL
                                                                R10
                                                                10$
                                         20$:
                   BA
05
             0E
                                                     POPR
                                                                #^M<R1,R2,R3>
                                                                                                     RESTORE REGISTERS
                                                     RSB
```

MB

SA SS

In Co Pa Sy Pa Sy Ps Cr As

- SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2 STARTIO - STARTIO OPERATION 1016 1017 .SBTTL STARTIO - STARTIO OPERATION STARTIO - START READ OPERATION ON SHARED MEMORY MAILBOX FUNCTIONAL DESCRIPTION: THIS ROUTINE IS ENTERED WHEN THE UNIT IS NOT BUSY AND THERE IS A PACKET TO PROCESS. IF THERE IS A MESSAGE WAITING IN THE MAILBOX: O THE MESSAGE IS DEQUEUED THE MAILBOX QUOTAS ARE ADJUSTED IF THE MESSAGE CONTAINED THE MESSAGE WRITER'S IRP SEQUENCE NUMBER, A MESSAGE IS SENT TO THE APPROPRIATE PROCESSOR TO TO INDICATE THE WRITE I/O SHOULD BE COMPLETED. o THE READ I/O REQUEST IS POSTED WITH THE ADDRESS OF THE MESSAGE IF THERE IS NO MESSAGE WAITING IN THE MAILBOX AND THE I/O REQUEST SPECIFIED IOSM_NOW, THE REQUEST IS COMPLETED WITH FAILURE (SS\$_ENDOFILE). 1039 1040 IF THERE IS NO MESSAGE WAITING IN THE MAILBOX AND THE I/O REQUEST 1041 DID NOT SPECIFY IOSM_NOW: O THE PORT'S WAITING READER FLAG (MBX\$W_READER) IS SET THE READ ATTENTION AST FLAGS (MBX\$W_READAST) FOR ALL PORTS ARE SCANNED AND IF SET, A MESSAGE IS SENT TO THE APPROPRIATE PROCESSOR TO INDICATE THAT THE AST'S SHOULD BE DELIVERED. 1047 1048 1049 O AN RSB TO THE DRIVERS CALLER IS EXECUTED LEAVING THE DRIVER TO AWAIT MESSAGE WRITTEN NOTIFICATION. 1052 1053 1054 1055 INPUTS: R3 = 1/0 PACKET ADDRESS R5 = UCB ADDRESS 1056 1057 1058 1059 1060 1061 1062 1063 1064 1065 1066 1067 1068 1069 OUTPUTS: R1 = OUR PORT NUMBER. R2 = FIRST MESSAGE BLOCK ADDRESS. R4 = MAILBOX ADDRESS. OTHERWISE AN RSB IS DONE. STARTIO: DO

54 0098 C5 50

MOVL UCB\$L MB MBX(R5),R4 SET_PORTFLAG MBX\$W_READER(R4) QRETRY SUCCESS=105,-REMOHI MBX\$Q_MSG(R4),R2 #SS\$_BADQUEUEHDR,RO MOVZWL CLRL REQCOM

GET MAILBOX ADDRESS SET THAT WE HAVE A READER ATTEMPT TO DEQUEUE A MESSAGE SET FAILURE STATUS COMPLETE THE READ REQUEST

To

TP

| | - SHARE | D MEMORY MAILBO - STARTIO OPER | X DEVICE DRIVER | 16-SEP-1984 12-SEP-1984 | 00:02:15 YA | X/VMS Macro V04-00 RIVER.SRCJMBXDRIVER.MAR; 2 | Page 24 (10) |
|---------------------------------|----------------------------------|---|--|---|---|--|--------------|
| | 04 04 04 04 04 | B9 1073 : IF A B9 1074 : IF A B9 1075 : IF I B9 1076 : ANY B9 1077 : TO W | MESSAGE WAS DEQU OSM NOW WAS SPECI INTERESTED PROCES WAIT FOR A MESSAGE | EUED, COMPLETE FIED EXIT WITH SORS THAT A RE TO BE WRITTEN | THE 1/0 REQ FAILURE. O ADER IS WAIT | UEST. OTHERWISE, THERWISE, NOTIFY ING AND JUST RETURN | |
| OD 20 A3 06 50 0870 8F 51 | 10 04 E1 04 30 04 04 04 | BB 1081 CO 1082 C5 1083 158. | BVC FINISHE BBC #10\$V N MOVZWL #SS\$_EN CLRL R1 REQCOM | EAD IOW, IRP\$W_FUNC(I IDOFFILE, RO | : | EAR, MESSAGE DEQUEUED CLEAR, WAIT LURE STATUS E THE READ REQUEST | |
| 0153 28 EE 50 | BB 04 30 04 BA 04 E9 04 | CD 1086 CF 1087 D2 1088 D4 1089 | PUSHR #^M <r3, BSBW NOTIFY POPR #^M<r3; BLBC R0,15\$</r3; </r3, | READER | : RESTORE | GISTERS PROCESSORS OF READER REGISTERS URE, EXIT AIT FOR MESSAGE NOTIFICAT | ION |

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- SHARED MEMORY MAILBOX DEVICE DRIVER
                                                                                    16-SEP-1984 00:02:15
12-SEP-1984 23:15:56
                                                                                                                      VAX/VMS Macro V04-00
[DRIVER.SRC]MBXDRIVER.MAR;2
                                                                                                                                                                    Page
                   FINISHREAD - FINISH READ I/O OPERATION
                                     1092
1093
1094
1095
                            04D8
04D8
04D8
04D8
                                                          .SBTTL FINISHREAD - FINISH READ I/O OPERATION
                                             ; FINISHREAD - FINISH READ OPERATION
                                                FUNCTIONAL DECRIPTION:
                                                THIS ROUTINE IS ENTERED WHEN A MESSAGE IS AVAILABLE FOR A READ I/O
                                                REQUEST.
                                                INPUTS:
                                                         R2 = FIRST MESSAGE BLOCK ADDRESS.
R3 = 1/0 REQUEST PACKET ADDRESS
                                                          R4 = MAILBOX ADDRESS.
                                                          R5 = UCB ADDRESS
                                                OUTPUTS:
                                     1109
                                             FINISHREAD:
                                                                                                            : CLEAR WAITING READER FLAG
                                                          CLR_PORTFLAG MBX$W_READER(R4)
                            04DF
04DF
                                               FORMAT MESSAGE BLOCKS FOR 1/0 POST
                                                                     #IRP$M_COMPLX!IRP$M_CHAINED,-; SET COMPLEX/CHAINED I/O
IRP$W_STS(R3)
R2,IRP$L_SVAPTE(R3)
IRP$L_MEDIA(R3),-
INSERT USER BUFFER ADDRESS
MSG_L_POSTUBUF(R2)
R2,R0
; GET FIRST BLOCK ADDRESS
                     A8
                                                          BISW
2C A32A
             A3
52
A3
A2
52
                     DO
                                                          MOVL
                                                                                                                INSERT BLOCK ADDRESS IN PACKET
        38
                           04E7
04E7
04EF
04EF
04F3
04F7
04F7
0503
                     DO
                                                          MOVL
                                     1120
1121
1122 10$:
1123
1124
1125
1126
1127
     50
                     DO
                                                          MOVL
                                                                      MSG_B_MESSAGE(RO),-
MSG_L_POSTIOBUF(RO)
MSG_L_CHAINLINK(RO),R1
RESTORE_QUOTAS
        10
                     9E
                                                          MOVAB
                                                                                                               INSERT ADDRESS OF DATA
51
        10
             AO
                    D0
13
C0
D0
D0
11
                                                          MOVL
                                                                                                               GET OFFSET TO NEXT BLOCK
                                                                                                               IF EQL NONE
COMPUTE ADDRESS OF BLOCK
SET ADDRESS AS LINK
GET NEW ADDRESS
             0C
52
51
51
                                                          BEQL
                                                                      R2,R1
                                                          ADDL
     A0
10
                                                                      R1, MSG_L_CHAINLINK(RO)
                                                          MOVL
                                                          MOVL
                                                                      R1,R0
             EA
                                                         BRB
                                               RESTORE MAILBOX QUOTAS
                                            RESTORE_QUOTAS:
                                                                                                               RESTORE MAILBOX QUOTAS
                                                                      #MBX$V QUOTALCK MBX$B_FLAGS(R4); LOCK MAILBOX QUOTAS
MBX$W_MSGCNT(R4); DECREMENT MESSAGE COUNT
MSG_W_MSGLENGTH(R2),- ; RESTORE BUFFER QUOTA
MBX$W_BUFFQUO(R4);
                                                         DECM
        16 A4
0E A2
18 A4
                     B7
                     AO
                                                          ADDW
                                                                      #MBX$7 QUOTALCK MBX$B_FLAGS(R4); UNLOCK MAILBOX QUOTAS MBX$W_MSGCNT(R4) = : SAVE MESSAGE COUNT UCB$L_DEVDEPEND(R5) ;
                                                          UNLOCK
                     B0
        16 A4
                                                          MOVW
                                     1142
1143
1144
1145
1146
1147
                                                NOTIFY WRITER THAT THE MESSAGE WAS READ (IF WRITER WANTED TO KNOW)
                                                AND REPORT MAILBOX RESOURCE AVAILABILITY.
                                                                      #^M<R2,R3,R4,R5>
UCB$L_MB_SHB(R5)
NOTIFY_READ
                                                                                                              SAVE REGISTERS
SAVE ADDRESS OF SHB
                                                          PUSHR
     009C C5
                                                          PUSHL
                                                          BSBW
                                                                                                               NOTIFY WRITER
```

Ta

NO

| | 50 02 00000000 • GF 3C | 8ED0 16 BA | 0540 0543 0546 0540 | 1149 1150 1151 1152 | MOVL POPL JSB POPR | #RSNS_MAILBOX,RO R1 G^MA\$RAVAIL #^M <r2,r3,r4,r5></r2,r3,r4,r5> | GET MAILBOX RESOURCE NUMBER RESTORE ADDRESS OF SHB REPORT THE RESOURCE AVAILABLE RESTORE REGISTERS |
|----|---|----------------------------|------------------------------|--------------------------------------|--|--|---|
| | | | 054E 054E | 1154 | : | | COMPLETE THE READ I/O REQUEST. |
| | 0E A2 32 A3 | B0 B1 | 054E 0553 | 1156 | MOVW | #SS\$ BUFFEROVF, RO IRP\$0_BCNT(R3), - | : Assume buffer overflow. |
| | 32 A3 OE A2 | 1F BO | 0558 055A | 1159 1160 1161 | BLSSU MOVW | MSG_W_MSGLENGTH(R2) 108 MSG_W_MSGLENGTH(R2), - | ; Was there a buffer overflow? ; Branch if buffer overflow. ; Otherwise, transfer only the |
| 50 | 10 10 32 A3 28 1C A2 05 50 0870 8F | B0 F0 91 12 B0 | 055F 0562 0568 056C | 1162 1163 1164 1165 1166 | 10\$: MOVW INSV CMPB BNEQ MOVW | #SS\$ NORMAL, RO IRP\$0 BCNT(R3), #16, # MSG_B_FUNC(R2), #IO\$_W 20\$ #SS\$_ENDOFFILE,RO | ; bytes actually in the message. ; and set normal xfer completed. 16, RO; Plant bytes transfered count. RITEOF; Was this an end-of-file function? ; Branch if not an end-of-file. |
| | 51 18 A2 | | 0573 0573 0577 | 1167 1168 1169 | 20\$: MOVL REQCOM | | ; Else, set eof status. ; GET EXTENDED PID OF WRITER ; COMPLETE READ I/O REQUEST |

0098

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- SHARED MEMORY MAILBOX DEVICE DRIVER MBX$INT - INTERRUPT DISPATCHER
                                                              16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
                                    .SBTTL MBX$INT - INTERRUPT DISPATCHER
                           MBXSINT - PORT REQUEST INTERRUPT DISPATCHER
                           FUNCTIONAL DESCRIPTION:
                                    THIS ROUTINE IS CALLED WHEN THE PORT DRIVER RECEIVES A REQUEST FROM A PROCESSOR THAT SPECIFIES THE MAILBOX
                                    DRIVER AS THE MESSAGE DISPATCHER ID (PROSC_MAILBOX).
                                    THIS ROUTINE EXAMINES THE REQUEST TYPE CODE AND DETERMINES WHETHER IT SHOULD:
                                       O DELIVER ALL THE WRITE ATTENTION AST'S FOR A UNIT AND COMPLETE ANY WAITING READ REQUEST(S) (PRQ_WRITE)
                                       O DELIVER ALL THE READ ATTENTION AST'S FOR A TIT BECAUSE A READER IS WAITING (PRO_READER)
                                       O COMPLETE A WRITE I/O REQUEST BECAUSE THE MESSAGE WRITTEN WAS READ BY ANOTHER PROCESS (PRQ_READ)
                           INPUTS:
                                    RO-R4 = SCRATCH.
                                    R5 = INTER-PROCESSOR REQUEST BLOCK ADDRESS.
                                    OO(SP) = ADDRESS OF IDB ADDRESS.
                                    IPL = IPLS_MAILBOX
                           OUTPUTS:
                                    APPROPRIATE ACTION IS TAKEN DEPENDING ON THE REQUEST TYPE.
                        MBX$INT:
                                                                                       INTERRUPT DISPATCHER
                                              a(SP)+,R3
R5,R2
PRQ$W_UNIT(R2),R5
IDB$L_UCBLST(R3)[R5],R5
INT_EXIT
UCB$L_MB_MBX(R5),R4
PRQ$W_REGTYPE(R2),-
LIMIT=#PRQ_READ,<-
                                                                                       GET ADDRESS OF IDB
SAVE REQUEST BLOCK ADDRESS
GET UNIT NUMBER OF REQUEST
GET UCB ADDRESS
                                    MOVL
 DO 300 1300 1300
                                    MOVL
                                    MOVZWL
                                    MOVL
                                                                                       IF EQL NO CORRESPONDING UNIT
GET MAILBOX ADDRESS
DISPATCH ON REQUEST TYPE
                                    BEQL
                                    MOVL
                                    CASE
                                                READ REQ. -
WRITE REQ. -
READER_REQ. -
                                                                                         MESSAGE WAS READ
MESSAGE WAS WRITTEN
                                                                                         READER WAITING
                        INT_EXIT:
                                                                                       EXIT INTERRUPT
 05
                           READER WAITING REQUEST - DELIVER ANY READ ATTENTION AST'S
                        READER_REQ:
                                                                                       READER WAITING REQUEST
                                    CLR_PORTFLAG MBX$W_READAST(R4) : CLEAR NOTIFY FLAG
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V

| | - SHARED ME | MORY MAILBOX DI | EVICE DRIVER | 16-SEP-1984 12-SEP-1984 | 00:02:1 | 15 VAX/VMS 56 EDRIVER. | Macro VO4-00 SRCJMBXDRIVER.MAR;2 | Page | 28 (12) |
|--|--|---|---|--|--------------------------------|--|---|-------|------------|
| 54 0094 C5 00000000 GF | DE 05A6 17 05AB | 1228 MG 1229 JI | OVAL UCBSL ME | RAST(R5),R4 | ; GE | | F READ AST LIST | | |
| | DE 05A6 17 05AB 05B1 05B1 05B1 05B1 05B1 | 1231 MESSAGE 1232 IF A RE 1233 DEQUEUE | WAS WRITTEN RE AD I/O REQUEST A MESSAGE AND | QUEST - DELIVIS ALREADY WAS COMPLETE THE | VER ANY AITING F I/O REG | WRITE ATTEN FOR A MESSAG DUEST. | TION AST'S AND | | |
| 54 0090 C5 00000000 GF 54 0098 C5 D1 64 A5 08 53 58 A5 | 05B1 05B1 05B8 16 05BD 00 05C3 E1 05C8 D0 05D1 | 1228 MI 1229 JI 1230 : MESSAGE 1231 : MESSAGE 1232 : IF A REA 1233 : DEQUEUE 1234 WRITE_REQ 1236 MI 1237 MI 1238 JI 1239 MI 1240 BI 1241 MI 1242 QI 1243 RI 1244 BI 1245 10\$: MI | : LR PORTFLAG MB) OVAL UCB\$L ME SB G^COM\$DE OVL UCB\$L ME BC #UCB\$V E OVL UCB\$L TF RETRY ERROR=10 | (\$W_WRITAST(RG) WAST(RS),R4 EATTNAST B_MBX(RS),R4 BSY,UCB\$W_STS(RP(RS),R3 | (R5) . INT | SSAGE WAS WEAR NOTIFY T ADDRESS OF THE MAILBOX AFT MAILBOX AFT TO DE | RITTEN REQUEST FLAG F WRITE AST LIST ST'S DDRESS AGAIN CLEAR, NO READER WA T ADDRESS QUEUE A MESSAGE MESSAGE DEQUEUED | ITING | |
| BA FEF1 | 1D 05E2 31 05E4 05E7 | 1245 1246 1246 1246 10\$: | EMQHI MBXSQ MS VS INT EXIT RW FINTSHRE | G(R4),R2 AD | I E | V-SET, NO SE, MESSAGE | MESSAGE DEQUEUED | | |
| 50 0394 8F 51 | 3C 05E7 04 05EC | 1247 MI 1248 CI 1249 RI | | QUEUEHDR,RO | ; SE | T FAILURE S | | | |
| | 05F4 05F4 05F4 | 1251 : MESSAGE | WAS READ REQUE | ST - COMPLETE | | | | | |
| 53 00A0 C5 51 53 | DE 05F4 DO 05F9 | 1253 ŘEAD_REQ: 1254 MC 1255 MC | OVAL UCB\$L_ME | 3_WIOQFL (R5) ,1 | R3 : GE | ESSAGE WAS R ET ADDRESS O AVE A COPY O | EAD REQUEST F WRITE PACKET LIST F IT | HEAD | |
| 53 63 51 53 9A 50 A3 24 A2 | 05FC D1 05FF 13 0602 D1 0604 | 1257 MC 1258 CI 1259 BI 1260 CI | OVL (R3),R3 MPL R3,R1 EQL INT EXIT | GNUM(R3),- | : EN | ND OF QUEUE? | F NEXT PACKET REQUEST GONE ORRECT REQUEST? | | |
| 50 50 10 50 50 10 50 51 38 A3 50 000000000 GF | 12 0609 0F 060B B0 060E 78 0612 B0 0616 D4 0619 7D 061B 17 061F | 1262 BI 1263 RI 1264 MC 1265 A: 1266 MC 1267 CI 1268 MC | EMQUE (R3),R3 OVW IRP\$W_B(SHL #16,R0,F OVW #SS\$_NOF | CNT(R3),R0 R0 RMAL,R0 | ; RE : GE : MC : SE : (N | T BYTE COUN DVE TO UPPER ET SUCCESS S NO PID) ET I/O STATU | FROM QUEUE T OF MESSAGE WORD TATUS S IN IRP WRITE REQUEST | | |

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MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15
NOTIFY OTHER PROCESSORS OF COND 12-SEP-1984 23:15:56
                                                                                                                                    VAX/VMS Macro V04-00
[DRIVER.SRC]MBXDRIVER.MAR; 2
                                                                    .SBTTL NOTIFY - NOTIFY OTHER PROCESSORS OF CONDITIONS
                                                         NOTIFY_READER - NOTIFY OTHER PROCESSORS OF A READER NOTIFY_READ - NOTIFY OTHER PROCESSOR THAT A MESSAGE WAS READ NOTIFY_WRITE - NOTIFY OTHER PROCESSORS THAT A MESSAGE WAS WRITTEN
                                    06255
06255
06255
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06255
06255
06255
06255
06255
06255
06255
06255
06255
06255
06255
06255
                                                          THESE ROUTINES ARE CALLED TO FORMAT AND SEND A REQUEST TO THE
                                                          MAILBOX DRIVERS ON OTHER PROCESSORS.
                                                          INPUTS:
                                                                    R2 = FIRST MESSAGE BLOCK ADDRESS (NOTIFY_READ ONLY)
R3 = I/O PACKET ADDRESS
                                                                    R5 = UCB ADDRESS
                                              1286
1287
1288
1289
1290
1291
1293
1294
1296
1296
1299
1300
1300
1300
1300
                                                          OUTPUTS:
                                                                    RO = SUCCESS/FAILURE.
                                                                    REQUEST(S) FORMATTED AND PASSED TO PORT DRIVER FOR DELIVERY
                                                                    TO REQUIRED PROCESSORS.
                                                                    RO,R1,R2,R3,R4,R5 DESTROYED
                                                         NOTIFY ANY INTERESTED PROCESSORS THAT A READER IS WAITING
                                                      NOTIFY_READER:
                                                                                                                            NOTIFY READER AVAILABLE
   52 50
            0098 C5
10 A2
51 03
1F
                                    0625
062A
062E
0633
0633
0633
                                                                                UCB$L_MB_MBX(R5),R2
MBX$W_READAST(R2),R0
#PRQ_READER,R1
                             B0
30
11
                                                                                                                            GET ADDRESS OF MAILBOX
GET PORT "'S TO NOTIFY
                                                                    MOVL
                                                                    MOVW
                                                                    MOVZWL
                                                                                                                            SET REQUEST TYPE
                                               305
                                                                                 NOTIFY
                                                                    BRB
                                                                                                                            NOTIFY THEM
                                              1306
1307
1308
1309
1310
1311
1313
                                                         IF IT WANTED TO KNOW, NOTIFY PROCESSOR THAT WROTE MESSAGE THAT IT
                                                         WAS READ.
                                                      NOTIFY_READ:
                                                                                                                            NOTIFY MESSAGE READ
                                                                                MSG_B_PORT(R2),#1,R0
#PRQ_READ,R1
MSG_C_IRPSEQ(R2),R2
NOTIFY
                    A2
01
50
        01
                0B
                             78
30
00
12
05
                                    0633
0638
0638
0637
0641
0642
0642
0642
0647
0648
                                                                    ASHL
                                                                                                                            GET PORT # NOTIFY
                                                                                                                            SET REQUEST TYPE
GET WRITER'S PACKET #
             51
                                                                    MOVZWL
        52
                14
                                                                    MOVL
                                              1314
1315
1316
1317
1318
1319
1321
13223
13225
13227
                                                                    BNEQ
                                                                                                                            IF NEQ - WRITER IS INTERESTED
                                                                    RSB
                                                                                                                            ELSE - JUST RETURN
                                                         NOTIFY ANY INTERESTED PROCESSORS THAT A MESSAGE WAS WRITTEN
                                                                                                                            NOTIFY MESSAGE WRITTEN
GET ADDRESS OF MAILBOX
GET PORT #'S TO NOTIFY
                                                      NOTIFY_WRITE:
            0098
12
0E
                                                                                UCBSL_MB_MBX(R5),R2
MBXSW_WRITAST(R2),R0
MBXSW_READER(R2),R0
                     C5
A2
A2
O2
                              00
30
A8
30
     52
                                                                    MOVL
                                                                    MOVZWL
                                                                    BISW
             51
                                                                                #PRQ_ORITE,R1
                                                                    MOVZWL
                                                                                                                            SET REQUEST TYPE
                                                         NOTIFY PROCESSOR(S) THAT A CONDITION HAS OCCURED
                                                      NOTIFY:
                                                                                                                         : NOTIFY PORT(S)
```

NO VO

- SHARED MEMORY MAILBOX DEVICE DRIVER

```
- SHARED MEMORY MAILBOX DEVICE DRIVER
MBXDRIVER
V04-001
                                                    - SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 NOTIFY - NOTIFY OTHER PROCESSORS OF COND 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
                                                                                                                                                  SAVE UCB ADDRESS
GET FORK BLOCK ADDRESS
                                                     DD
DO
DO
                                                                                           PUSHL
                                                            0658
0658
06658
06655
06657
06677
0688
0688
0693
                                                                                                        IRPSL EXTEND(R3),R5
IRPSL EXTEND(R5),-
IRPSL EXTEND(R3)
                                55
                                                                                           MOVL
                                                                                           MOVL
                                                                                                                                                   REMOVE BLOCK FROM LIST
                                                     12
AA
                                                                      BNEQ
                                                                                                                                                   IF NEQ NOT LAST BLOCK
                                     0800
                        2A A3
                                                                                                        #IRPSM_EXTEND, IRPSW_STS(R3); ELSE, CLEAR EXTEND FLAG
                                                                                           BICW
                                                                              105:
                                                                                                                                                  SAVE PORT #'S TO NOTIFY
SAVE REQUEST TYPE
SAVE PARAMETER
                                                                                                        RO, IRPE$W_MB_PORTS(R5) ; SAVE PORT #'S TO R1, IRPE$W_MB_RQTYP(R5) ; SAVE REQUEST TYPE R2, IRPE$L_MB_PARAM(R5) ; SAVE PARAMETER #IPL$_MAILBOX, FKB$B_FIPL(R5) ; SET FORK IPL
                                18
1A
                                    A5
A5
A5
                                                  80
80
90
8EDO
90
9A
C3
00
                                                                                           MOVW
                                                                                           MOVW
                                 10
                                                                                           MOVL
                                             ÓB
50
                                0B
                                                                                           MOVB
                                                                                                                                                  RESTORE UCB ADDRESS
GET SHB ADDRESS
                                                                                           POPL
                                                                                                       009C
                             51
                                              CŎ
                                                                                           MOVL
                                             A1
C1
O1
                                51
                                         04
                                                                                           MOVL
                                     009C
                                                                                           MOVZBL
                         20 A5
                                                                                           SUBL 3
                                         24
38
                                             AO
A4
                                                                                           MOVL
                                                                                           MOVL
                                                                                 FORMAT PROCESSOR REQUEST MESSAGE AND RETURN TO PORT DRIVER FOR
                                                                                 DELIVERY TO OTHER PROCESSOR.
                                                            0693
0693
0693
                                                                              FORMAT_PRQ:
                                                                                                                                                  FORMAT PROCESSOR REQUEST
                                                                                                        IRPESU_MB_PORT(R5),-
IRPESW_MB_PORTS(R5),10$
                              20 A5
2E 18 A5
00000000 GF
                                                                                                                                                  IF CLR, DON'T NOTIFY THE PORT
                                                      E1
                                                                                           BBC
                                                            0696
0699
                                                      16
                                                                                                        G^MASREQUEST
                                                                                           JSB
                                                                                                                                                   CALL PORT DRIVER FOR A REQUEST BLOCK
                                                            069F
069F
                                                                                                                                                  R2 = MESSAGE BLOCK ADDRESS
IF LBC, FAILURE
                                                                                                       RO, NOTIFY DONE
IRPE$L MB PORT(R5),-
PRQ$W TO PORT(R2)
#PRQ$C MAILBOX,-
PRQ$W DISPATCH(R2)
IRP$L UCB(R3),R0
UCB$W UNIT(R0),-
                                             50
A5
A2
                                         20
20
18
                                                                                           BLBC
                                                            06A2
06A5
06A7
                                                      B0
                                                                                           MOVW
                                                                                                                                                  SET PORT NUMBER TO SEND TO
                                              01
                                                     B0
                                                                                           MOVW
                                                                                                                                                  SET MESSAGE DISPATCHER ID
                                                            06A9
06AB
06AF
06B2
06B7
06B9
06BE
06C2
06C4
06C7
                                        1C A2
1C A3
54 A0
22 A2
1A A5
20 A2
1C A5
24 OB
07 50
                                                                                           MOVL
                                                                                                                                                  GET UCB ADDRESS
                                                     BO
                                                                                           MOVW
                                                                                                                                                  SET UNIT NUMBER
                                                                                                        PRQSW UNIT(R2)
IRPESD MB RQTYP(R5),-
PRQSW REQTYPE(R2)
IRPESC MB PARAM(R5),-
PRQSL PARAM(R2)
                                                     B0
                                                                                           MOVW
                                                                                                                                                  SET REQUEST TYPE
                                                     DO
                                                                                                                                                  SET PARAMETER
                                                                                           MOVL
                                                                                                        #IPLS MAILBOX, FKBSB_FIPL (R2) ; SET DISPATCH IPL
                                                     90
16
E9
                                 OB A2
                                                                                           MOVB
                                                                                                                                               : RETURN TO PORT DRIVER FOR DELIVERY
                                                                                           JSB
                                                                                           BLBC
                                                                                                        RO, NOTIFY_DONE
                                                                                                                                               ; IF LBC, FAILURE
                                                                              105:
                                    C8 20 A5
                                                                                                        IRPE$L MB_PORT(R5), FORMAT_PRQ ; DECREMENT PORT # AND LOOP #SS$_NORMAL, RO ; SET SUCCESS
                                                      F4
                                                                                           SOBGEQ
                                                            06CE
06CE
06CE
06CE
06CE
                                                                                           MOVL
```

DONE WITH NOTIFICATION, DEALLOCATE THE FORK BLOCK

G^EXESDEANONPAGED

RO R5,R0

DONE WITH NOTIFICATION

SET ADDRESS OF BLOCK

RESTORE EXIT STATUS

DEALLOCATE FORK BLOCK

SAVE EXIT STATUS

NOTIFY_DONE:

00000000 GF

06D0 06D3

06D9

06DC

D0 16

PUSHL

MOVL

JSB

POPL

RSB

```
- SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 ALLOC_IRPE - ALLOCATE AN I/O REQUEST PAC 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER
                                                                                                                          [DRIVER.SRC]MBXDRIVER.MAR:2
                                           1383
1384
1385
1386
1387
1388
                                  06DD
                                                               .SBTTL ALLOC_IRPE - ALLOCATE AN I/O REQUEST PACKET EXTENSION
                                  0600
                                                      ALLOC_IRPE - SUBROUTINE TO ALLOCATE AN I/O REQUEST PACKET EXTENSION
                                  06DD
                                                      THIS ROUTINE IS CALLED TO ALLOCATE AN I/O REQUEST PACKET EXTENSION FOR LATER USE AS A FORK BLOCK.
                                  06DD
                                  06DD
                                  06DD
                                  06DD
                                                      INPUTS:
                                  06DD
                                  06DD
                                                               R3 = I/O PACKET ADDRESS.
                                  06DD
                                  06DD
06DD
                                           1394
1395
                                                      OUTPUTS:
                                           1396
1397
                                  06DD
                                                               IRPE ALLOCATED FROM NON-PAGED POOL AND LINKED TO END
                                                               OF I/O PACKET (IRP$L_EXTEND). IF ALLOCATION FAILS, ANY PREVIOUSLY ALLOCATED IRPE IS DEALLOCATED AND THE PROCESS IS PUT IN RESOURCE WAIT STATE TO AWAIT NON-PAGED POOL
                                  06DD
                                           1398
1399
                                  06DD
                                  06DD
                                  06DD
                                           1400
                                                               AVAILABILITY.
                                  06DD
                                           1401
                                           1402
1403
1404
                                  06DD
                                                   ALLOC_IRPE:
                                                                                                                ; ALLOCATE AN IRPE
                                  06DD
                                                               PUSHL
                                                                                                                   SAVE REGISTER
                                                                           WIRPSK LENGTH R1
GEXESALONONPAGED
            00C4 8F
                                  06DF
                                                               MOVZWL
                                                                                                                   SET SIZE OF BLOCK
      00000000 GF
                                  06E4
                                            1405
                                                                                                                                ALLOCATE BLOCK
                                                                JSB
                                           1406
                         8ED0
                                  06EA
                                                                                                                   RESTORE REGISTER
                                                               POPL
                                                                           RO,20$

R1,IRPE$W_SIZE(R2)

#DYN$C_IRPE,IRPE$B_TYPE(R2); SET BLOCK TYPE IN BLOCK
IRP$L_EXTEND(R3),-

IRPE$C_EXTEND(R2)

IRPE$C_EXTEND(R2)
                            E9
B0
90
                                  06ED
06F0
06F4
                26
                                            1407
                                                               BLBC
        SA 80
                                            1408
                                                               MOVW
                                            1409
                                                               MOVB
                54
                            90
                                  06F8
                                           1410
                                                               MOVL
                                           1411
1412
1413
1414 10$:
                                  06FB
                            13
A8
                                  06FD
                                                               BEQL
                                                                                                                    IF EQL NONE
                                                                           #IRPESM_EXTEND, IRPESW_STS(R2); SET EXTENSION FLAG
2A A2
            0800
                                                               BISW
2A A3 54
                                                                           R2, IRP$L_EXTEND(R3) ; SET IRPE ADDRESS IN IRP
#IRP$M_EXTEND, IRP$W_STS(R3) ; SET EXTENSION FLAG
IRPE$L_SVAPTE1(R2) ; CLEAR SVAPTE SO I/O POST WILL
IRPE$L_SVAPTE2(R2) ; JUST DEALLOCATE THE BLOCKS
            A3 52
0800 8F
                                           1415
                            DO A8 D4 D4 D5
                                                               MOVL
                                           1416
                                                               BISW
                2C A2
                                  070F
                                                               CLRL
                                  0712
0715
                                                               CLRL
                                                               RSB
                                           1420
1421
1422
1423
1424
                                  0716
                                                   20$:
                            CO
3C
3C
31
                                                                           #4,SP
#SS$ INSFMEM,-(SP)
#RSN$ NPDYNMEM,R1
                                  0716
                                                               ADDL
                                                                                                                   REMOVE RETURN ADDRESS
             0124 8F
51 03
                                                               MOVZWL
                                                                                                                   SET FAILURE STATUS
                                                                                                                   SET RESOURCE TO AWAIT
                                                               MOVZWL
                 FD37
                                                               BRW
                                                                            RES_WAIT
                                                                                                                   WAIT FOR NON-PAGED POOL
```

```
- SHARED MEMORY MAILBOX DEVICE DRIVER 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 DALLOC_IRPE - DEALLOCATE AN I/O REQUEST 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
                                                          .SBTTL DALLOC_IRPE - DEALLOCATE AN I/O REQUEST PACKET EXTENSION
                                                 DALLOC_IRPE - SUBROUTINE TO DEALLOCATE AN I/O REQUEST PACKET EXTENSION
                                                 INPUTS:
                                                         R3 = I/O REQUEST PACKET ADDRESS.
                                                 OUTPUTS:
                                                         THE I/O REQUEST PACKET EXTENSION IS DEALLOCATED TO NON-PAGED POOL.
                                       1439 : 1440 : 1441 : 1442 : -- 1443 DALLO 1444 1445 1446 1447 1448 1449 1450 10$: 1451 1452 1453 1454 20$: 1456 MB_EM
                                                         R1,R3,R5 ARE PRESERVED.
                                              DALLOC_IRPE:
                                                                                                         DEALLOCATE AN IRPE
                                                                     IRP$L_EXTEND(R3),R0
                         D0
13
D0
                                                         MOVL
                                                                                                         GET IRPE ADDRESS
       50
                                                                                                         BR IF NONE
              54
                                                                     IRPESL EXTEND(RO),-
                                                                                                         REMOVE IRPE FROM LIST
                                                          MOVL
                         12
AA
                                                         BNEQ
                                                                                                          IF NEQ NOT LAST IRPE
2A A3
           0800
                                                                     #IRPSM_EXTEND, IRPSW_STS(R3); CLEAR EXTEND FLAG
                        BB
16
BA
                                                                     #^M<R1,R3>
G^EXE$DEANONPAGED
#^M<R1,R3>
                                                                                                      : SAVE REGISTERS
: DEALLOCATE IRPE
: RESTORE REGISTERS
                                                          PUSHR
     00000000 GF
                                                         JSB
POPR
                         05
                                                          RSB
                                              MB_END:
```

.END

| MBXDRIVER Symbol table | - SHARED MEMORY MAILBOX DEVICE DRIVER | 16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 Page 33 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2 (15) |
|---------------------------|--|---|
| | = 00000020 R 02 EXESQIORYPK 0000002EC R 03 EXESRADCHK 00000436 R 03 EXESMITECHK 00000003 FDTEOF = 00000002 FDTREAD = 00000002 FDTWRITE = 00000001 FINISHREAD 00000078 R 03 FK85B-FPL = 00000008 FK85K-LENGTH FORMAT PRQ 00000038 FUNCTABLEN 0000038 R 03 FUNCTABLEN 00000383 R 03 IDSL UTBLS ******** X 03 IDST NORSWAI ******** X 03 IDST NORSWAI ******* X 03 IDST NORSWAI ****** X 03 IDST NORSWAI ******* X 03 IDST NORSWAI ******** X 03 IDST NORSWAI ******* X 03 IDST NORSWAI ******** X 03 IDST NORSWAI ******* X 03 IDS | |

| BXDRIVER ymbol table | - SHARED MEMORY | MAILBOX DEVICE DRIVER | 16-SEP-1984 00:02:15 VAX/VMS 12-SEP-1984 23:15:56 EDRIVER | Macro V04-00 Page 34 .SRC]MBXDRIVER.MAR;2 (19 |
|---|--|---|---|--|
| RPESL_SVAPTE1 RPESL_SVAPTE2 RPESM_EXTEND RPESW_MB_PORTS RPESW_MB_RQTYP RPESW_SIZE RPESW_STS NMSDECETE_LNMB | = 0000002C = 00000038 = 00000800 00000018 0000001A = 00000008 = 0000002A | PCB\$L_EPID PCB\$L_PHD PCB\$L_PID PCB\$L_UIC PMS\$GL_MBREADS PMS\$GL_MBWRITES PR\$_IPC 03 PRQ\$C_MAILBOX 03 PRQ\$C_MINLENGTH | = 00000064 = 00000060 = 00000080 | |
| RPESW_MB_RQTYP RPESW_SIZE RPESW_STS NMSDECETE_LNMB | = 0000001A = 0000002A | PMSSGL_MBREADS PMSSGL_MBWRITES PRS_IPC 03 PRQSC_MAILBOX | = 00000012 = 0000001 | X 03 X 03 |
| MMSUNLOCK ASRAVAIL ASREQUEST | ******* X | 03 PRQSC_MAILBOX 03 PRQSC_MINLENGTH 03 PRQSL_PARAM 03 PRQSW_DISPATCH 03 PRQSW_REQTYPE 03 PRQSW_TO PORT | = 00000040 = 00000024 = 00000010 = 00000020 | |
| ASKH ASKL | 00000443 R = 00000100 = 00000000 = 00000009 | PRQ\$W_TO_PORT PRQ\$W_UNIT PRQ_READ PRQ_READER | = 00000018 = 00000022 = 00000001 = 00000003 | |
| BXSB_CREATPORT BXSB_FLAGS BXSDDT BXSINT BXSM_VALID | = 00000100 = 00000000 = 00000009 = 00000008 00000000 RG 0000057D R | PRQ WRITE OR PRVSV BYPASS OR READCRECKIO READER REQ | = 00000002 = 000001D 0000018D R | 03 |
| SXSQ MSG BXSV QUOTALCK BXSW BUFFQUO BXSW MSGCNT BXSW PROT BXSW READAST | = 00000002 = 00000003 = 00000018 = 0000001A | PROSC MINLENGTH OS PROSU PARAM OS PROSW DISPATCH PROSW REGTYPE OS PROSW UNIT PROSW UNIT PRO READER PRO WRITE OS PROST BYPASS OS READCRECKIO READER REQ RESTORE QUOTAS RES WAIT RSNS MAILBOX RSNS NPDYNMEM SETUP BLOCK SHBSL DATAPAGE SHBSL REFCNT SHDSB FLAGS OS SHDSW MBXQUOTA SHDSW RESWAIT | 000005F4 R 00000505 R 0000045B R | 03 03 03 03 |
| XSW_PROT IXSW_READAST IXSW_READER | = (1111(1111111111111111111111111111111 | RSNS NPDYNMEM SETUP BLOCK SHB\$L DATAPAGE | = 00000003 00000332 R = 00000004 | 03 |
| READER REF REF REF REF REF REF REF | = 0000000E = 0000000C = 00000012 00000742 R | SHD\$B FLAGS SHD\$B PORTS SHD\$V MBXLCK | = 0000000C = 0000009C = 00000003 | |
| G_B_MESSAGE G_B_PORT G_B_TYPE G_L_CHAINLINK | 00000010 | SHDSW_MBXQUOTA SHDSW_RESWAIT SHMRES_WAIT SSS_ABORT | = 0000005C = 000000A8 00000459 R = 0000002C | 03 |
| GGLTRPSEQ GGLTPID GGLTPOSTIOBUF GGLTPOSTUBUF | 00000014 00000018 00000000 00000004 | SHDSW_MBXQUOTA SHDSW_RESWAIT SHMRES_WAIT SS\$_ABORT SS\$_BADQUEUEHDF SS\$_BUFFEROVF SS\$_ENDOFFILE SS\$_INSFMEM SS\$_MBFULL SS\$_MBFULL SS\$_NOPRIV SS\$_NOPRIV SS\$_NOPRIV | = 00000394 = 00000601 = 00000870 = 00000124 | |
| G B MESSAGE G B PORT G B TYPE G L CHAINLINK G L IRPSEQ G L PID G L POSTIOBUF G L POSTUBUF G Q MSGLINK G W LENGTH G W SIZE | 0000000 0000000 0000000E | SS\$_MBFULL SS\$_MBTOOSML SS\$_NOPRIV | = 000008D8 = 0000019C = 00000024 = 00000001 | |
| TTET | 00000652 R 000006CE R 00000633 R | 03 STARTIO 03 TOOSMALL 03 UCB\$B_DEVCLASS 03 UCB\$B_DEVTYPE 03 UCB\$B_DIPL | 00000491 R 000001DF R = 00000040 | 03 03 |
| OTIFY DONE OTIFY READ OTIFY READER OTIFY WRITE RBSB_FLAGS RBSL_OWNER RBSM_PROT_16 | 0000000B 00000010 00000014 00000018 00000000 00000000 00000000 0000000E 000000 | OS TOOSMALL OS UCB\$B_DEVCLASS OS UCB\$B_DEVTYPE UCB\$B_DIPL UCB\$B_FIPL UCB\$K_MB_LENGTH UCB\$L_CRB UCB\$L_CRB UCB\$L_DEVCHAR UCB\$L_DEVCHAR2 UCB\$L_DEVCHAR2 UCB\$L_IRP UCB\$L_IRP UCB\$L_MB_MBX | = 00000012 = 00000024 = 00000020 = 00000020 = 00000022 = 00000003 = 00000003 = 000000059F R 0000059F R 0000059F R 0000059F R 0000059F R 00000059F R 000000059F R 000000050 R = 000000050 R = 0000000050 R = 000000050 R = 000000000 R = 000000000 R = 0000000000 | |
| Pag-PROT | = 00000008 = 0000001 = 00000018 = 0000000 | UCB\$L_CRB UCB\$L_DEVCHAR UCB\$L_DEVCHAR2 UCB\$L_DEVDEPEND | = 00000024 = 00000038 = 0000003C = 00000044 | |
| | = 00000008 = 0000000C | UCB\$L_IRP UCB\$L_LOGADR UCB\$L_MB_MBX | = 00000058 = 00000074 = 00000098 | |

```
- SHARED MEMORY MAILBOX DEVICE DRIVER
 MBXDRIVER
                                                                                                                                                                   16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 12-SEP-1984 23:15:56 [DRIVER.SRC]MBXDRIVER.MAR;2
 Symbol table
UCB$L_MB_PORT
UCB$L_MB_RAST
UCB$L_MB_SHB
UCB$L_MB_WAST
UCB$L_MB_WIOQFL
UCB$L_ORB
UCB$L_STS
UCB$M_DELETEUCB
UCB$M_DELETEUCB
UCB$W_DELMBX
UCB$V_DELMBX
UCB$V_DELMBX
UCB$V_ONLINE
UCB$W_DEVBUFSIZ
UCB$W_DEVSTS
UCB$W_DEVSTS
UCB$W_TEFC
                                                                         000000A8
0000009C
0000009O
000000A0
000000AC
00000064
00000008
00000008
00000001
00000004
00000042
00000068
                                                                      =
                                                                      =
                                                                          00000008
                                                                          000002DC
0000019F
                                                                                                           03
03
03
WRITECHECKIO
WRITE REQ
ZEROLENGTH
                                                                          000005B1
                                                                          000001D5
                                                                                                               Psect synopsis !
PSECT name
                                                                                                                     PSECT No.
                                                                                                                                             Attributes
                                                                        Allocation
                                                                                                                                                                                                                                                           WRT NOVEC BYTE WRT NOVEC BYTE WRT NOVEC BYTE WRT NOVEC LONG
       ABS
                                                                        00000000
                                                                                                                     00
                                                                                                                                 0.)
                                                                                                                                             NOPIC
                                                                                                                                                               USR
                                                                                                                                                                                                       LCL NOSHR
                                                                                                                                                                                                                            NOEXE
                                                                                                                                                                                                                                          NORD
                                                                                                                                                                                                                                                        NOWRT
                                                                                                                                                                                          ABS
                                                                        00000024
00000076
00000742
                                                                                                                                             NOPIC
                                                                                                                    01
02
03
                                                                                                                                                                                                                                 EXE
                                                                                                                                                                                                      LCL NOSHR
 $ABS$
                                                                                                                                 1.)
                                                                                                                                                               USR
                                                                                                                                                                            CON
                                                                                                                                                                                          ABS
                                                                                                                                                                                                                                              RD
$$$105_PROLOGUE
$$$115_DRIVER
                                                                                                                                             NOPIC
                                                                                                                                                               USR
                                                                                                                                                                            CON
                                                                                                                                                                                         REL
                                                                                                                                                                                                                                              RD
                                                                                                                                                               USR
                                                                                                         Performance indicators
Phase
                                                        Page faults
                                                                                          CPU Time
                                                                                                                           Elapsed Time
 ----
                                                                                         00:00:00.06
00:00:00.37
00:00:18.16
00:00:02.64
00:00:04.07
00:00:00.16
00:00:00.01
00:00:00.01
                                                                                                                           00:00:01.48
 Initialization
                                                                                                                           00:00:01.48
00:00:03.65
00:01:03.39
00:00:12.33
00:00:14.48
00:00:00.29
00:00:00.01
00:00:00.00
 Command processing
                                                                        605
 Pass 1
Symbol table sort
                                                                       271
29
2
Pass 2
Symbol table output
Psect synopsis output
 Cross-reference output
                                                                      1060
 Assembler run totals
```

The working set limit was 2100 pages.
153019 bytes (299 pages) of virtual memory were used to buffer the intermediate code.
There were 130 pages of symbol table space allocated to hold 2476 non-local and 78 local symbols.
1457 source lines were read in Pass 1, producing 21 object records in Pass 2.
55 pages of virtual memory were used to define 52 macros.

MBXDRIVER - SHARED MEMORY MAILBOX DEV

16-SEP-1984 00:02:15 VAX/VMS Macro V04-00 Page 36

! Macro library statistics !

Macro library name

_\$255\$DUA28:[SYS.OBJ]LIB.MLB:1 _\$255\$DUA28:[SYSLIB]STARLET.MLB;2 TOTALS (all libraries) Macros defined

37 10 47

2794 GETS were required to define 47 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LISS:MBXDRIVER/OBJ=OBJS:MBXDRIVER MSRCS:MBXDRIVER/UPDATE=(ENHS:MBXDRIVER)+EXECMLS/LIB

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